500 Series OPT. K

SCHEMATIC

(COPY)

A35-J3

SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
S1VM	OPT	24	1	31-P5	H
S2VM	↑	↑	2	↑	J
S4VM			3		K
S3VM			4		L
SD1M			5		M
SD2M			7		N
SD4M			8		P
SD3M			10		R
SDV1			11		S
SDV2			12		T
SDV4			13		U
SDV3			14		V
SPUR			15		B
SV01			16		E
SV02			17		F
SV03			18		H
SIR1			20		W
SIR2			21		X
SIR3			22		Y
SIR4			23		Z
SIR5			24		A
SIR6			25		B
SDPC			26		F
FCOV			27		D
WDLS			28		A
WRST			29		E

SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
SPRR			51	31-P5	M
			52		
			53		
			54		
			55		
+30V	3	18	56	29-P1	A
↑	↑	↑	57	↑	C
↓	↓		58	↓	E
+30V	3		59	29-P1	H
PS RET	0		60	BUS	BAR
↑	↑		62	↑	↑
↓	↓		63		
P.S. RET	0		64		
CH. GND.	5		65	↓	↓
CH. GND.	5		66	BUS	BAR
+30V	3		67	29-P1	K
↑	↑		70	↑	M
			71		P
			72		S
↓	↓		73	↓	U
+30V	3		74	29-P1	W
PS. RET	0		75	BUS	BAR
↑	↑		76	↑	↑
			77		
			78		
			79		

A35-J2

SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
$\overline{MCA0}$	OPT	24	1	47-P1	L
$\overline{MCA1}$	↑	↑	2	↑	M
$\overline{MCA2}$			3		N
$\overline{MCA3}$			4		P
$\overline{MCA4}$			5		R
$\overline{MCA5}$			7		S
$\overline{MCA6}$			8		T
$\overline{MCA7}$			10		U
$\overline{MCA8}$	↓	↓	11	↓	V
$\overline{MCA9}$	OPT	24	12	47-P1	W
			13		
			14		
			15		
			16		
			17		
			18		
			20		
			21		
			22		
			23		
			24		
			25		
			26		
			27		
			28		
			29		

SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
			51		
			52		
			53		
$\overline{SMCA}$	OPT	24	54	47-P1	A
$\overline{SMCB}$	↑	↑	55	↑	B
$\overline{SMCC}$			56		C
$\overline{SMCD}$			57		D
$\overline{SMCE}$	OPT	24	58	47-P1	E
			59		
			60		
			62		
			63		
			64		
			65		
			66		
			67		
			70		
			71		
			72		
			73		
			74		
			75		
			76		
			77		
			78		
			79		

A47-P1

SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
SMCA	OPT	24	A	35-J2	54
SMCB	↑	↑	B	↑	55
SMCC	|	|	C	|	56
SMCD	↓	↓	D	↓	57
SMCE	OPT	24	E	35-J2	58
			F		
			H		
			J		
			K		
MCA0	OPT	24	L	35-J2	1
MCA1	|	↑	M	↑	2
MCA2	|	|	N	|	3
MCA3	|	|	P	|	4
MCA4	|	|	R	|	5
MCA5	|	|	S	|	7
MCA6	|	|	T	|	8
MCA7	|	|	U	|	10
MCA8	↓	↓	V	↓	11
MCA9	OPT	24	W	35-J2	12
			X		
			Y		
			Z		
RVCC	OPT	24	a	29-P1	Ⓒ
			b		
			c		
			d		

SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
			A		
			B		
			C		
			D		
			E		
			F		
			H		
			J		
			K		
			L		
			M		
			N		
			P		
			R		
			S		
			T		
			U		
			V		
			W		
			X		
			Y		
			Z		
			a		
			b		
			c		
			d		

A33-P6

SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
$\overline{SHFO}$	OPT	24	A	35-J3	41
$\overline{SHFI}$	↑	↑	B	↑	42
$\overline{SSBO}$	↑	↑	C	↑	47
$\overline{SSBI}$	↑	↑	D	↑	48
$\overline{SHIO}$	↑	↑	E	↓	49
$\overline{SHII}$	↑	↑	F	35-J3	50
$\overline{SZRI}$	↑	↑	H	35-J2	40
$\overline{SZR2}$	↑	↑	J	35-J2	41
$\overline{SHRO}$	↑	↑	K	35-J2	45
$\overline{STIQ}$	↑	↑	L	35-J3	40
$\overline{SZR3}$	↑	↑	M	35-J2	42
$\overline{SZR4}$	↓	↓	N	35-J2	43
$\overline{SZR5}$	OPT	24	P	35-J2	44
			R		
			S		
			T		
			U		
			V		
			W		
			X		
			Y		
			Z		
			a		
			b		
			c		
			d		
			e		

SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
			A		
			B		
			C		
			D		
			E		
			F		
			H		
			J		
			K		
			L		
			M		
			N		
			P		
			R		
			S		
			T		
			U		
			V		
			W		
			X		
			Y		
			Z		
			a		
			b		
			c		
			d		
			e		

A29-P1 30 LOGIC SUPPLY

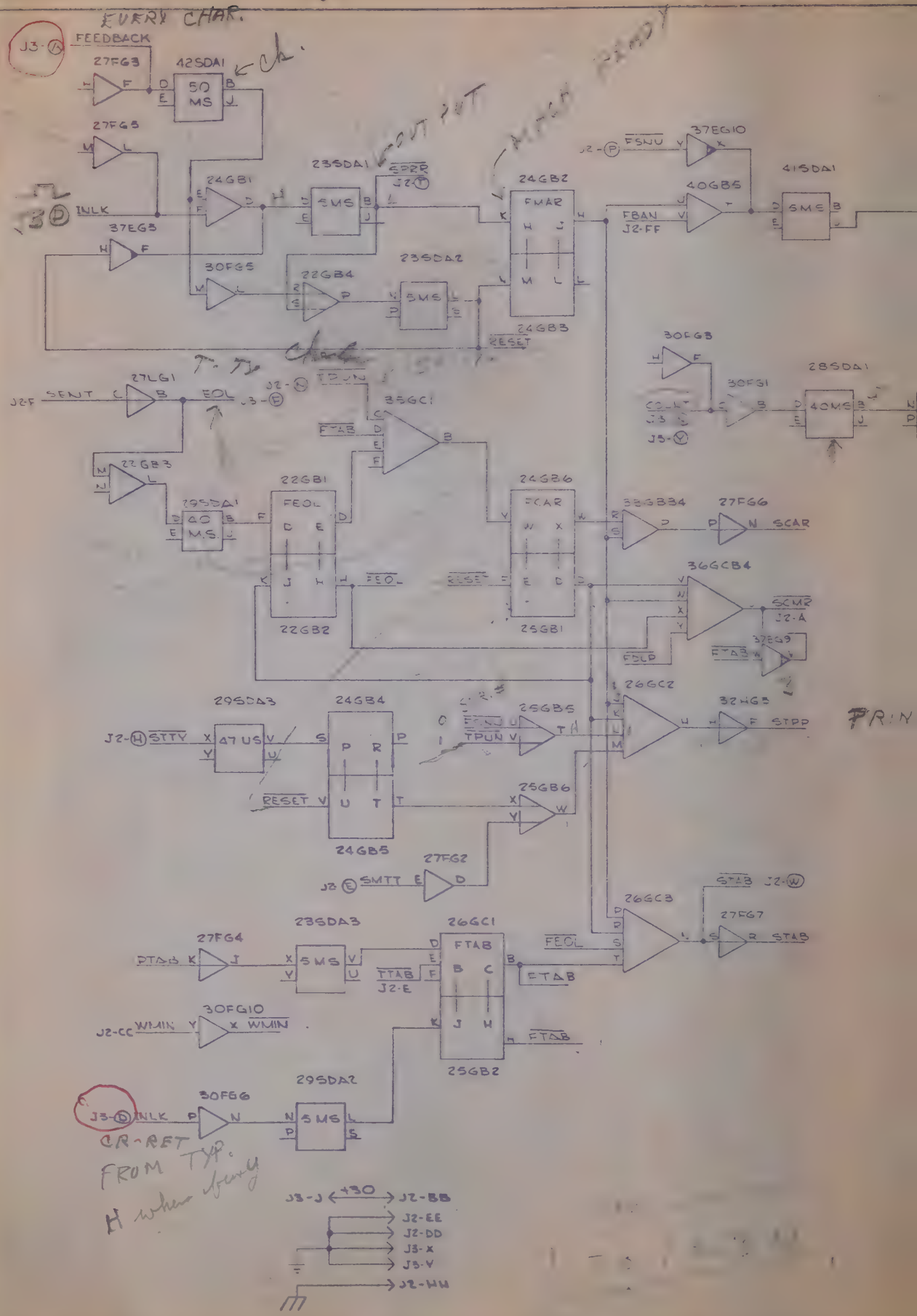
SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
~~FCOV~~ ~~+30V~~	3	18	A	~~31-P5~~	~~C~~
P.S. RET	0		B	BUS	BAR
+30V	3		C	35-J3	57
~~RVCC~~	0		D	~~47-P1~~	~~A~~
+30V	3		E	35-J3	58
P.S. RET	0		F	BUS	BAR
+30V	3		H	35-J3	59
P.S. RET	0		J	BUS	BAR
+30V	3		K	35-J3	67
P.S. RET	0		L	BUS	BAR
+30V	3		M	35-J3	70
P.S. RET	0		N	BUS	BAR
+30V	3		P	35-J3	71
P.S. RET	0		R	BUS	BAR
+30V	3		S	35-J3	72
P.S. RET	0		T	BUS	BAR
+30V	3		U	35-J3	73
P.S. RET	0		V	BUS	BAR
+30V	3		W	35-J3	74
P.S. RET	0		X	BUS	BAR
+30V	3		Y	47-P1	AA
P.S. RET	0	18	Z	BUS	BAR
~~FCOV~~	~~OPT~~	~~24~~	a	~~31-P5~~	~~C~~
			b		
~~RVCC~~	~~OPT~~	~~24~~	c	~~47-P1~~	~~A~~
			d		
			e		

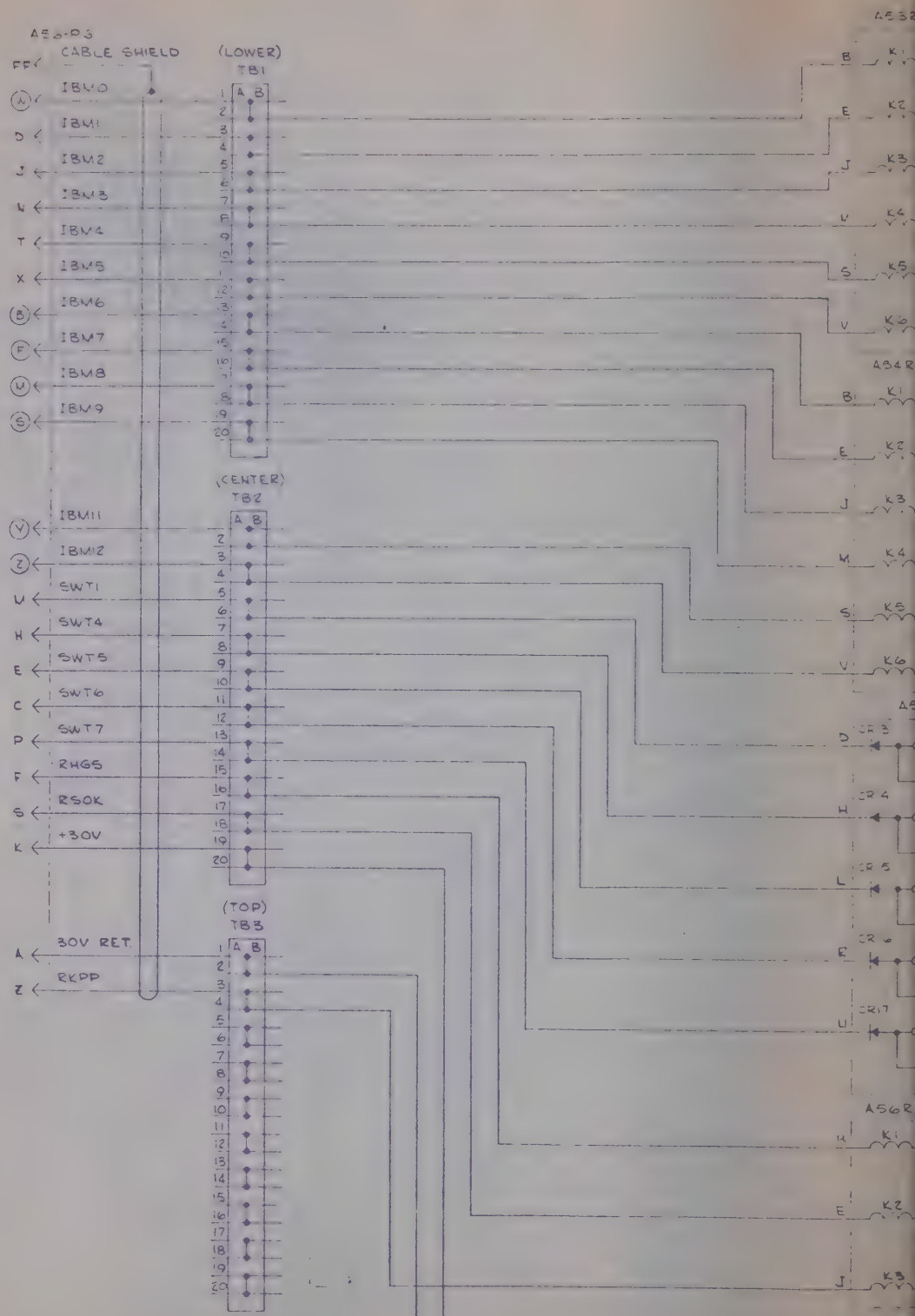
REVERSED TO START WITH PIN HH INSTEAD OF A

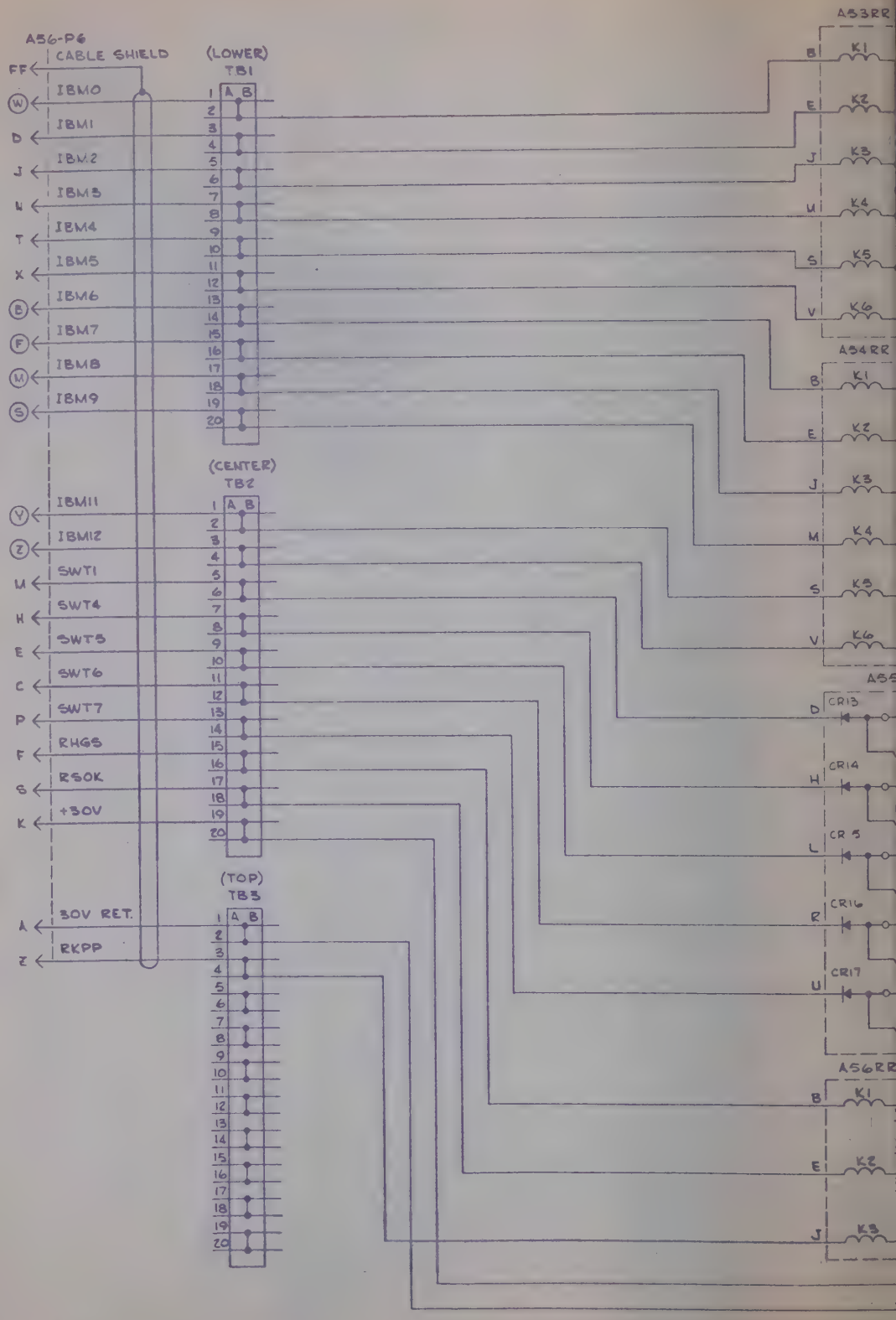
A31 P5 LOGIC I

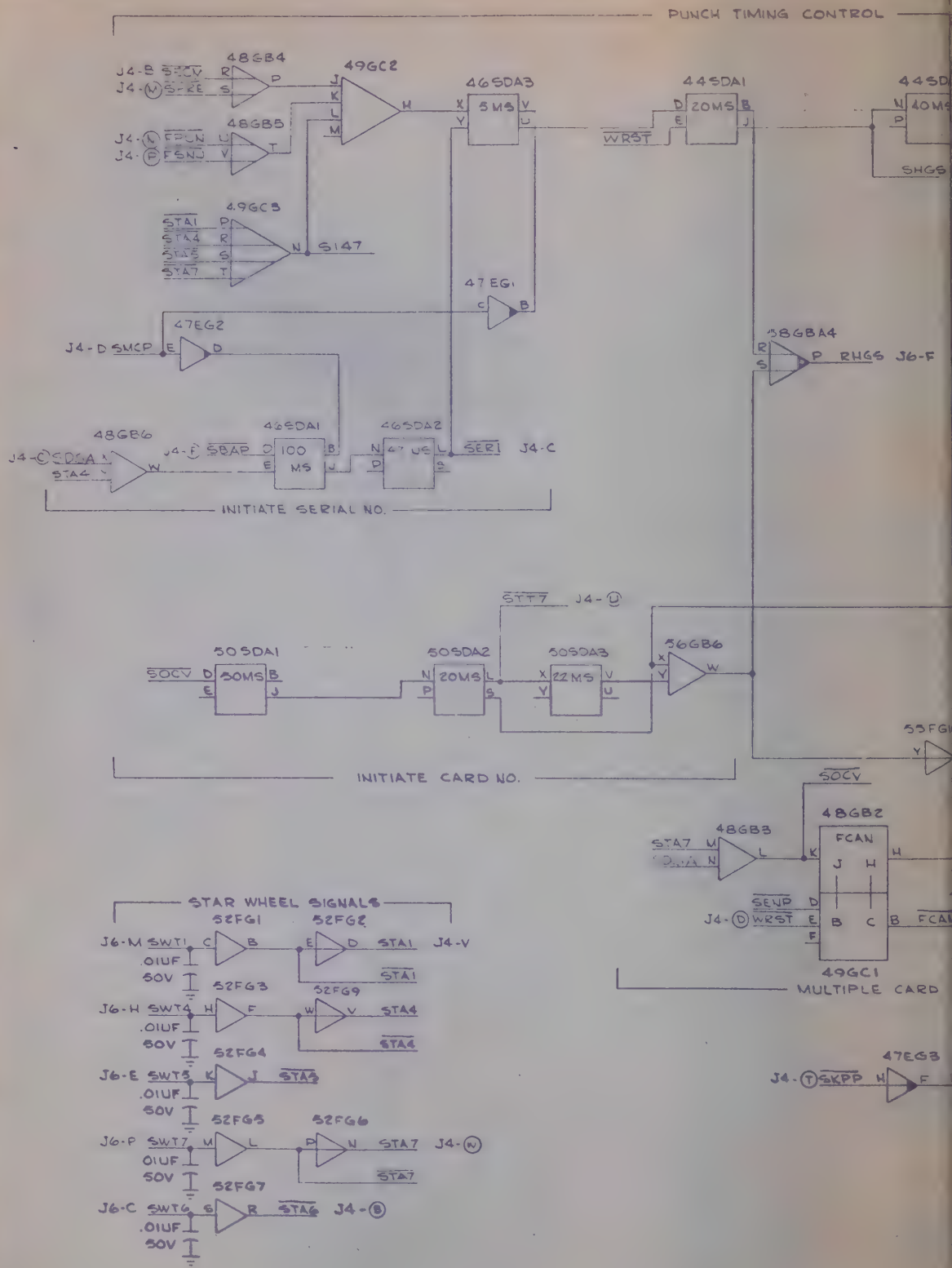
SIGNAL	COLOR	AWG	FROM PIN	TO	
					PIN
WDLS			A	35-J3	28
SPUR OUT			B		15
SECO			C		30
FCOV			D		27
WRST			E		29
SDPC			F		26
S1VM	0		H		1
S2VM			J		2
S4VM			K		3
S3VM			L		4
SD1M			M		5
SD2M			N		7
SD4M			P		8
SD3M			R		10
SDV1			S		11
SDV2			T		12
SDV4			U		13
SDV3			V		14
SIR1			W		20
SIR2			X		21
SIR3			Y		22
SIR4			Z		23
SIR5			a		24
SIR6			b	35-J3	25
FCOV			c	29-P1	A
SIRQ			d	35-J3	46
SIFQ1			e		16

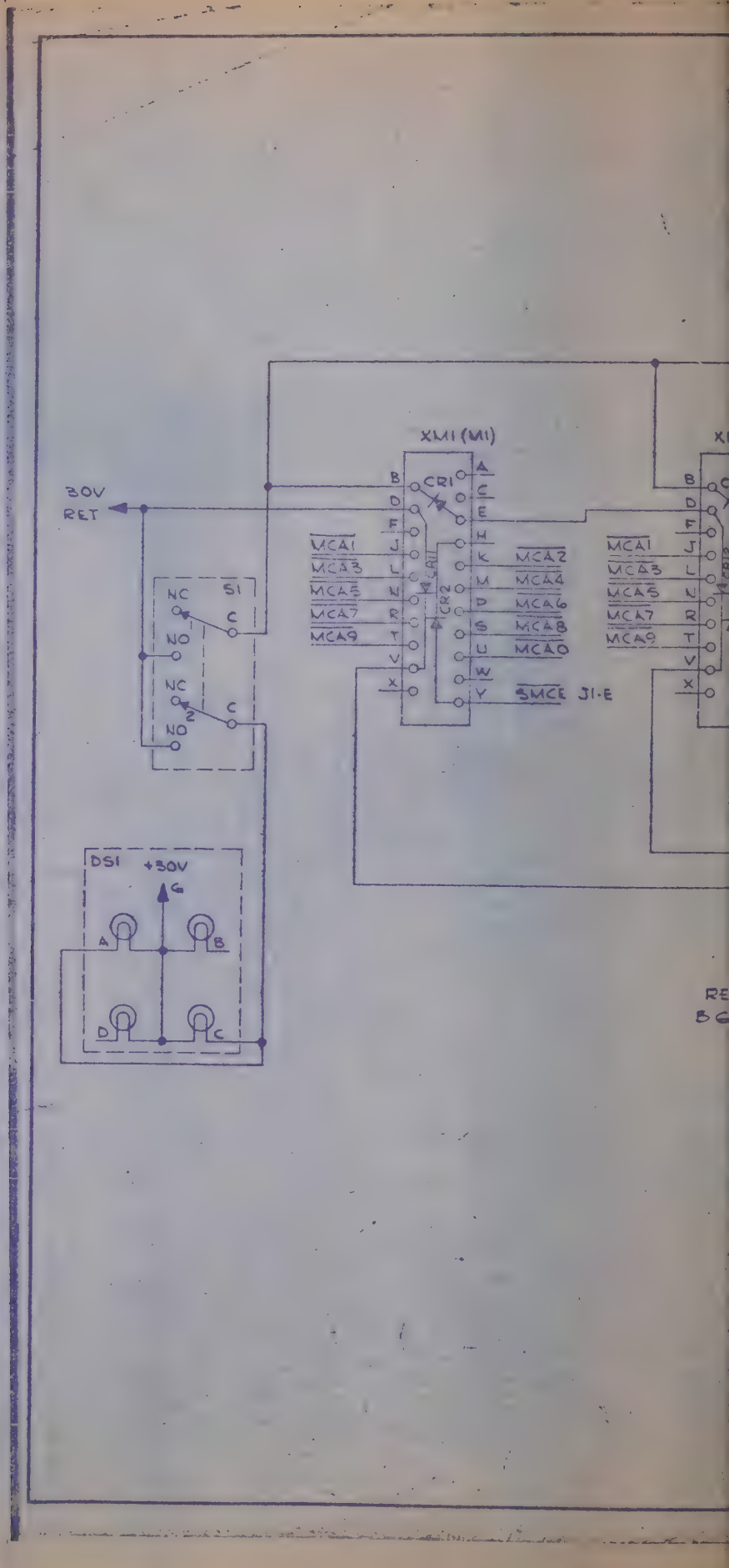
THIS IS ALSO REVERSED


EVERY CHAR.
J3-A
FEEDBACK
27FG3
12SDA1
50 MS
27FG3
24GB1
INLK
37EG3
30FG5
22GB4
23SDA1
23SDA2
24GB2
FMAR
24GB3
RESET
37EG10
FSNU
40GB5
41SDA1
FBAN
J2-FF
30FG3
28SDA1
40MS
27LG1
SENT
EOL
J3-E
TPUN
35GC1
FTAB
22GB3
22GB1
FEOL
29SDA1
40 MS
22GB2
24GB6
FCAR
25GB1
33GB34
27FG6
SCAR
36GCB4
SCMR
J2-A
37EG9
FTAB
FDLP
26GC2
32HG3
STPP
PRIN
29SDA3
47 US
STTY
24GB4
P R
U T
24GB5
RESET
25GB5
FENU
TPUN
25GB6
27FG2
SMTT
STAB J2-W
27FG7
STAB
26GC3
FEOL
FTAB
23SDA3
5 MS
TTAB
J2-E
26GC1
FTAB
B C
J K H
25GB2
27FG4
PTAB
30FG10
WMIN
29SDA2
30FG6
INLK
CR-RET
FROM TYP.
H when Ready
J3-J
430
J2-BB
J2-EE
J2-DD
J3-X
J3-V
J2-HH

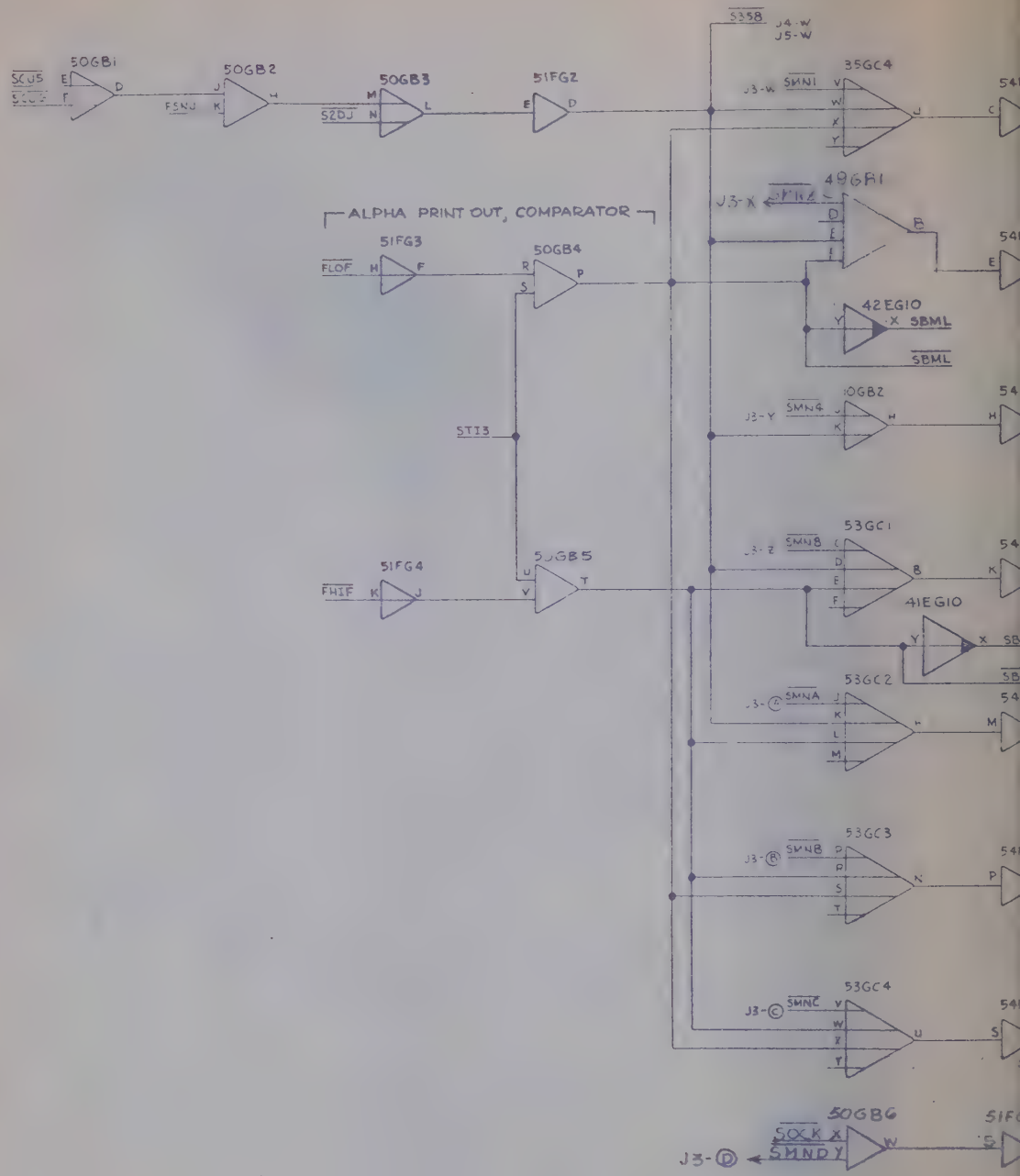

A53-P3
CABLE SHIELD
FF
(LOWER)
TB1
IBM0
IBM1
IBM2
IBM3
IBM4
IBM5
IBM6
IBM7
IBM8
IBM9
(CENTER)
TB2
IBM11
IBM12
SWT1
SWT4
SWT5
SWT6
SWT7
RHGS
RSOK
+30V
(TOP)
TB3
30V RET
RKPP
A53
A54R
A56R
K1
K2
K3
K4
K5
K6
CR3
CR4
CR5
CR6
CR17

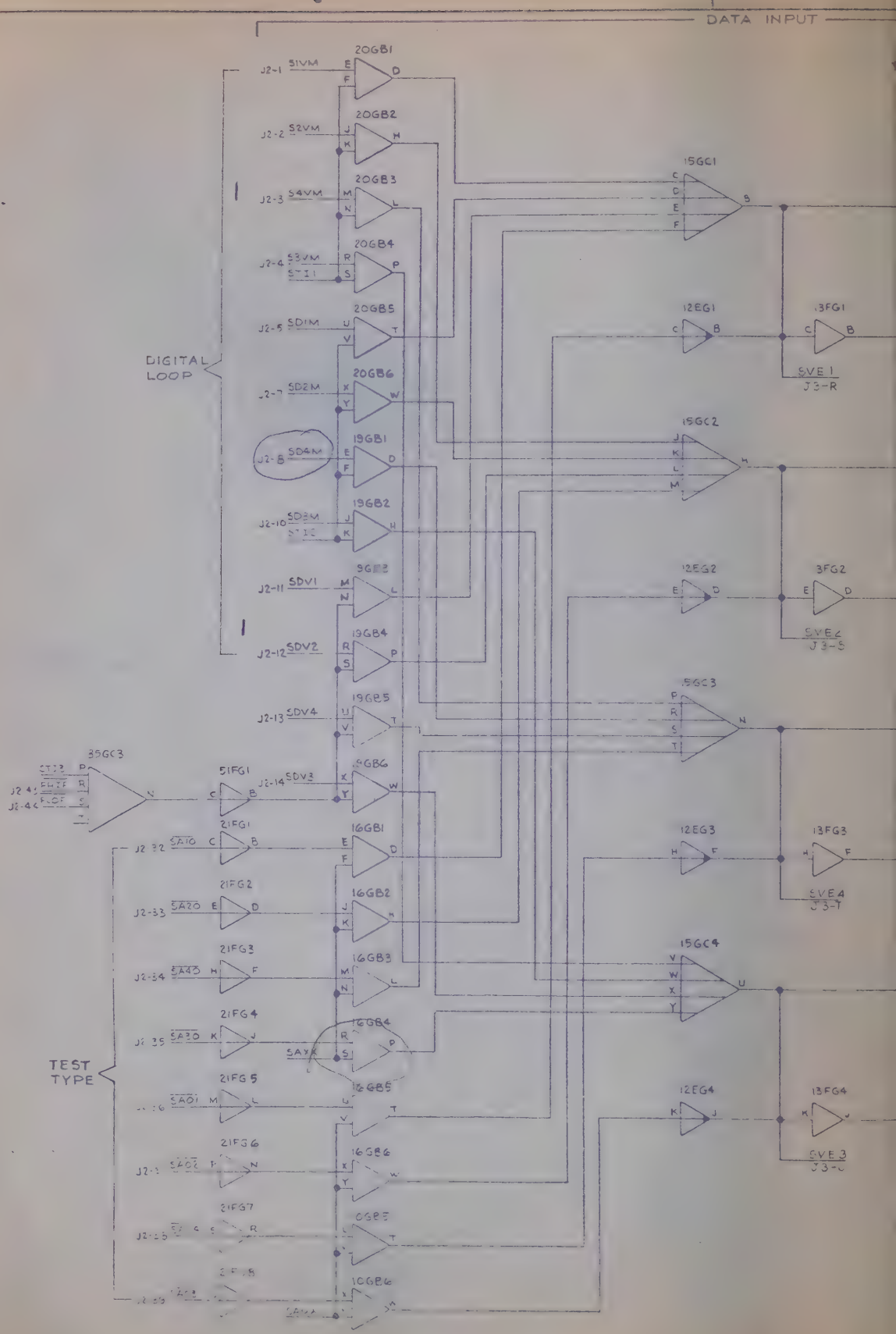

A56-P6
CABLE SHIELD
FF
IBM0
IBM1
IBM2
IBM3
IBM4
IBM5
IBM6
IBM7
IBM8
IBM9
IBM11
IBM12
SWT1
SWT4
SWT5
SWT6
SWT7
RHGS
RSOK
+30V
30V RET.
RKPP
(LOWER)
TB1
(CENTER)
TB2
(TOP)
TB3
A53RR
A54RR
A55
A56RR
K1
K2
K3
K4
K5
K6
CR13
CR14
CR 15
CR16
CR17

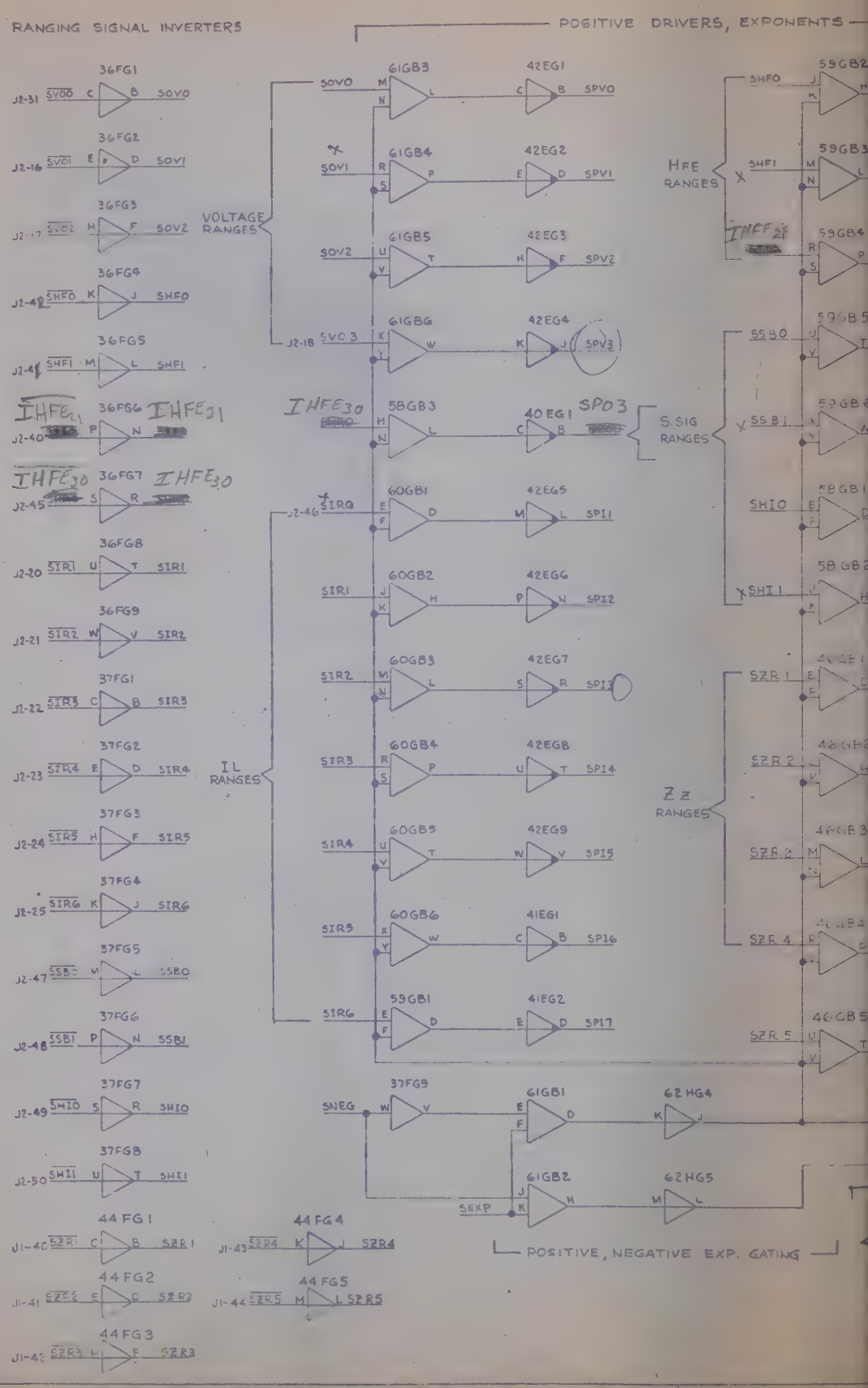

PUNCH TIMING CONTROL
48GB4
49GC2
46SDA3
5 MS
44SDA1
20MS
WRST
J4-B SRCV
J4-M SRE
48GB5
J4-N FPUN
J4-P FSNJ
49GC3
STA1
STA4
STA5
STA7
S147
47EG1
47EG2
J4-D SMCP
58GBA4
RHGS J6-F
48GB6
J4-C SDSA
STA4
46SDA1
100 MS
J4-E SBAP
46SDA2
47 MS
SER1 J4-C
INITIATE SERIAL NO.
STT7 J4-U
50SDA1
SOCV
50MS
50SDA2
20MS
50SDA3
22 MS
56GB6
55FG1
INITIATE CARD NO.
SOCV
48GB2
FCAN
48GB3
STA7
49GC1
MULTIPLE CARD
SENP
J4-D WRST
STAR WHEEL SIGNALS
52FG1
52FG2
J6-M SWT1
.01UF
50V
J4-V
STA1
52FG3
52FG9
J6-H SWT4
STA4
52FG4
J6-E SWT5
STA5
52FG5
52FG6
J6-P SWT7
STA7 J4-N
52FG7
J6-C SWT6
STA6 J4-B
47EG3
J4-T SKPP

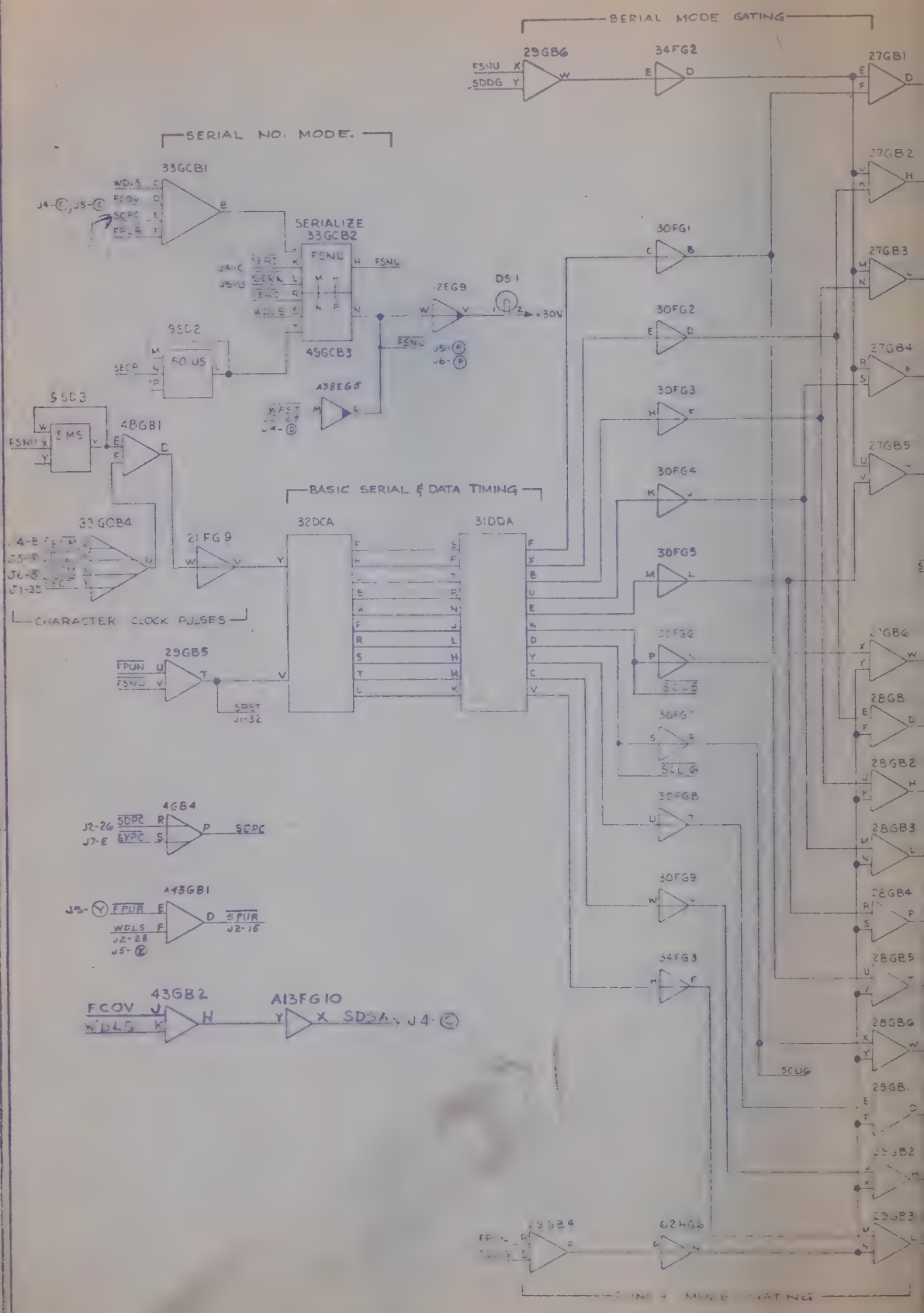

XM1 (M1)
30V
RET
S1
NC
C
NO
NC
C
NO
2
DS1
+30V
G
A
B
D
C
B
D
F
J
L
N
R
T
V
X
CR1
CR2
MCA1
MCA3
MCA5
MCA7
MCA9
A
C
E
H
K
M
P
S
U
W
Y
MCA2
MCA4
MCA6
MCA8
MCA0
SMCE J1-E

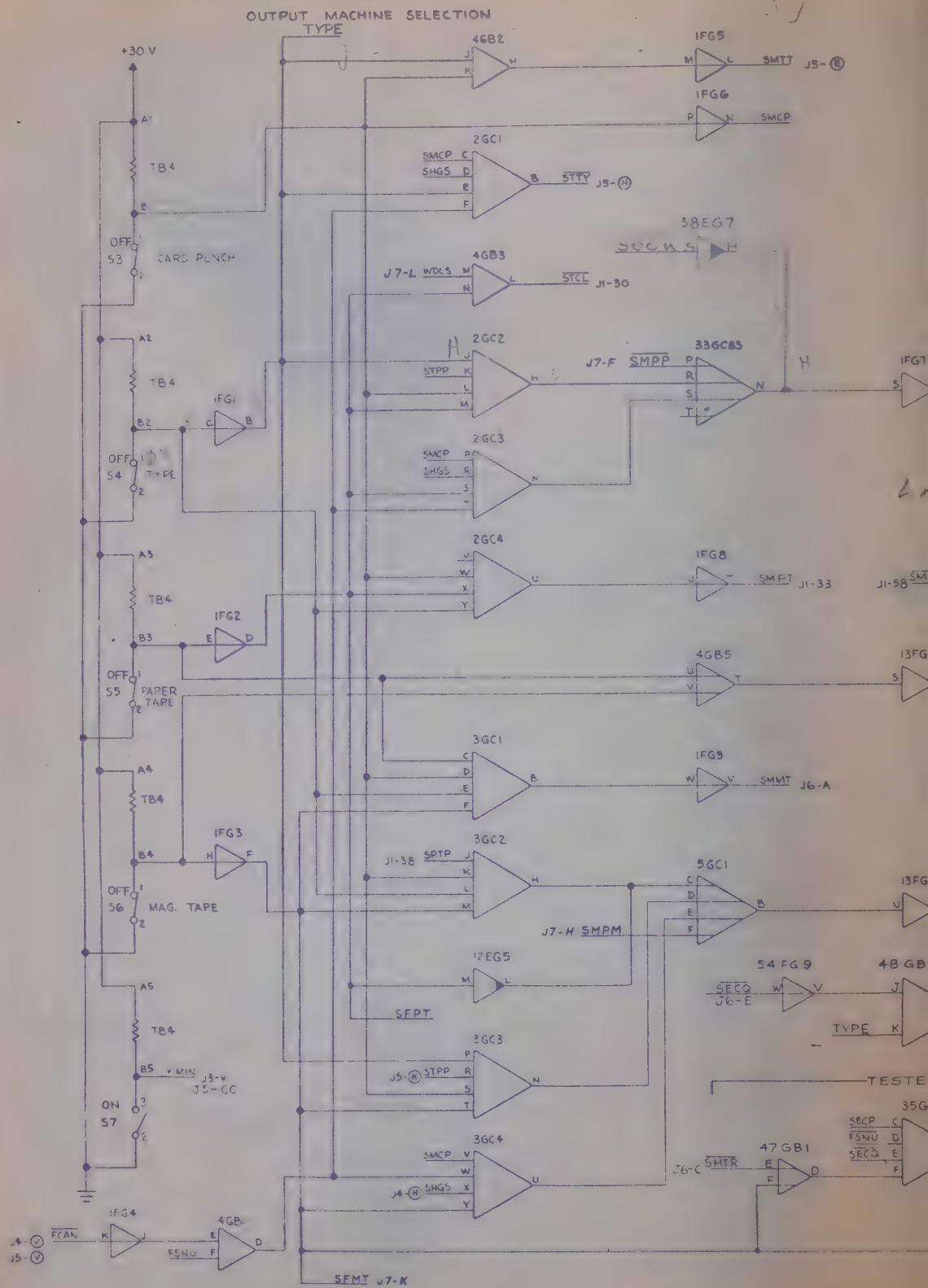
BCD OUTPUT FOR PAPER & MAG



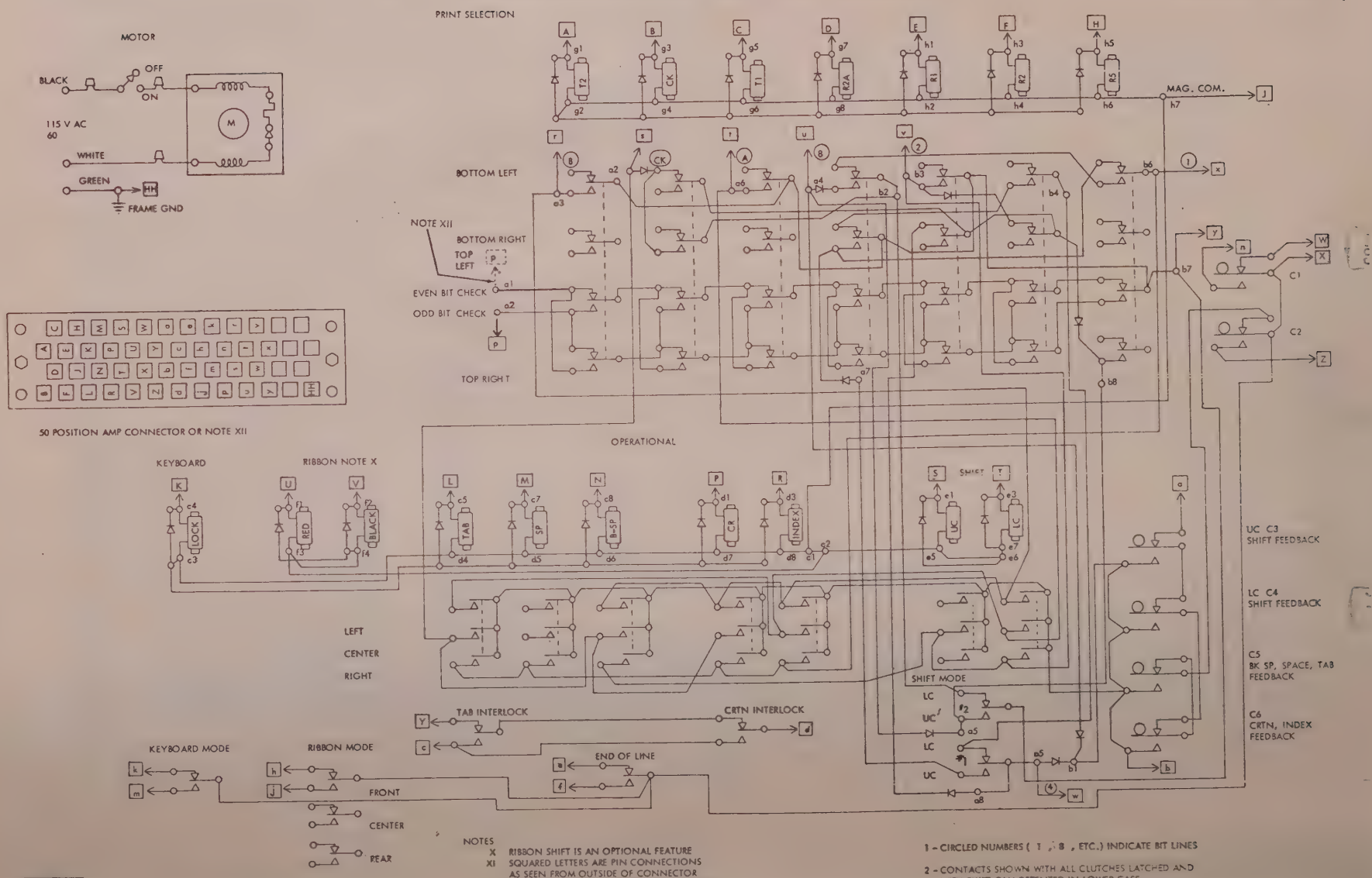

DATA INPUT
DIGITAL LOOP
TEST TYPE
20GB1
20GB2
20GB3
20GB4
20GB5
20GB6
19GB1
19GB2
19GB3
19GB4
19GB5
19GB6
16GB1
16GB2
16GB3
16GB4
16GB5
16GB6
16GB7
16GB8
15GC1
15GC2
15GC3
15GC4
12EG1
12EG2
12EG3
12EG4
13FG1
13FG2
13FG3
13FG4
35GC3
51FG1
21FG1
21FG2
21FG3
21FG4
21FG5
21FG6
21FG7
J2-1 S1VM
J2-2 S2VM
J2-3 S4VM
J2-4 S3VM
J2-5 SD1M
J2-7 SD2M
J2-8 SD4M
J2-10 SD3M
J2-11 SDV1
J2-12 SDV2
J2-13 SDV4
J2-14 SDV3
SVE1 J3-R
SVE2 J3-S
SVE4 J3-T
SVE3 J3-U


RANGING SIGNAL INVERTERS
POSITIVE DRIVERS, EXPONENTS
36FG1
36FG2
36FG3
36FG4
36FG5
36FG6
36FG7
36FG8
36FG9
37FG1
37FG2
37FG3
37FG4
37FG5
37FG6
37FG7
37FG8
37FG9
44FG1
44FG2
44FG3
44FG4
44FG5
VOLTAGE RANGES
IL RANGES
HFE RANGES
S.SIG RANGES
Z z RANGES
61GB3
61GB4
61GB5
61GB6
5BGB3
60GB1
60GB2
60GB3
60GB4
60GB5
60GB6
59GB1
42EG1
42EG2
42EG3
42EG4
40EG1
42EG5
42EG6
42EG7
42EGB
42EG9
41EG1
41EG2
59GB2
59GB3
59GB4
59GB5
59GB6
58GB1
58GB2
46GB1
46GB2
46GB3
46GB4
46GB5
37FG9
61GB1
62HG4
61GB2
62HG5
POSITIVE, NEGATIVE EXP. GATING


SERIAL MODE GATING
29GB6
34FG2
27GB1
FSNU
SDDG
SERIAL NO. MODE.
33GCB1
WDLS
FCOV
SCPC
FPUR
SERIALIZE
33GCB2
FSNU
49GCB3
9SC2
40 US
SECP
12EG9
DS1
+30V
30FG1
30FG2
30FG3
30FG4
30FG5
30FG6
30FG7
30FG8
30FG9
34FG3
27GB2
27GB3
27GB4
27GB5
27GB6
28GB
28GB2
28GB3
28GB4
28GB5
28GB6
29GB.
29GB2
29GB3
A38EG5
XRST
9SD3
SMS
48GB1
FSNU
BASIC SERIAL & DATA TIMING
33GCB4
21FG9
32DCA
31DDA
CHARACTER CLOCK PULSES
29GB5
FPUN
FSNU
SDRST
J1-32
4GB4
J2-26 SDPC
J7-E SVPC
SCPC
A13GB1
J5-Y FPUR
WDLS
J2-28
J5-E
SPUR
J2-16
43GB2
FCOV
WDLS
A13FG10
SDSA, J4-C
SCUG
62HG6
SERIAL MODE GATING


OUTPUT MACHINE SELECTION
TYPE
+30 V
A1
TB4
B1
OFF
S3
CARD PUNCH
A2
TB4
B2
OFF
S4
TYPE
A3
TB4
B3
OFF
S5
PAPER TAPE
A4
TB4
B4
OFF
S6
MAG. TAPE
A5
TB4
B5
ON
S7
1FG1
1FG2
1FG3
1FG4
4GB2
4GB3
2GC1
2GC2
2GC3
2GC4
3GC1
3GC2
3GC3
3GC4
4GB5
1FG5
1FG6
1FG7
1FG8
1FG9
38EG7
33GC85
5GC1
12EG5
54FG9
48GB
35G
47GB1
SMCP
SHGS
STTY
STCI
STPP
SMPP
SMFT
SMMT
SPTP
SMPM
SECQ
SMTR
SFPT
SFMT
TESTE
TYPE
J7-L WDLS
J7-F
J7-H
J1-38
J1-30
J1-33
J1-58
J5-K
J5-H
J5-CC
J4-V
J5-V
J6-A
J6-E
J6-C
J4-H
J7-K

BCD WIRING DIAGRAM



NOTES

X RIBBON SHIFT IS AN OPTIONAL FEATURE

XI SQUARED LETTERS ARE PIN CONNECTIONS AS SEEN FROM OUTSIDE OF CONNECTOR

XII MAY BE WIRED AS SHOWN FOR EVEN BIT CHECK

1 – CIRCLED NUMBERS (1 , 8 , ETC.) INDICATE BIT LINES

2 – CONTACTS SHOWN WITH ALL CLUTCHES LATCHED AND WITH SHIFT CAM DETENTED IN LOWER CASE

3– Alphabetic characters in squares (A a , etc.) correspond to

EC 522295-A
2528-B

SELECTION AND TILT-ROTATE SCHEDULE

BCD KEYBOARD

UC	LC	T2	CK	T1	R2A	R1	R2	R5	TLT	ROT
=	A	0	1	0	1	1	1	1	3	-5
B	B	0	1	0	1	0	1		3	+1
C	C	0	0	0	1	0	1	1	3	-4
D	D	0	1	0	1	1	0		3	+2
E	E	0	0	0	1	1	0	1	3	-3
F	F	0	0	0	1	0	0		3	+3
?	G	0	1	0	1	0	0	1	3	-2
Ø	H	0	1	0	0	1	0	1	3	-1
[	I	0	0	0	0	1	0		3	+4
J	J	0	0	1	1	1	1	1	2	-5
K	K	0	0	1	1	0	1		2	+1
L	L	0	1	1	1	0	1	1	2	-4
M	M	0	0	1	1	1	0		2	+2
N	N	0	1	1	1	1	0	1	2	-3
O	O	0	1	1	1	0	0		2	+3
!	P	0	0	1	1	0	0	1	2	-2
*	Q	0	0	1	0	1	0	1	2	-1
]	R	0	1	1	0	1	0		2	+4
S	S	1	0	0	1	0	1		1	+1
T	T	1	1	0	1	0	1	1	1	-4
U	U	1	0	0	1	1	0		1	+2
V	V	1	1	0	1	1	0	1	1	-3
W	W	1	1	0	1	0	0		1	+3
+	X	1	0	0	1	0	0	1	1	-2
%	Y	1	0	0	0	1	0	1	1	-1
}	Z	1	1	0	0	1	0		1	+4
(	1	1	1	1	1	1	1	1	0	-5
2	1	1	1	1	1	0	1		0	+1
3	1	0	1	1	0	1	1	1	0	-4
4	1	1	1	1	1	1	0		0	+2
5	1	0	1	1	1	1	0	1	0	-3
6	1	0	1	1	1	0	0		0	+3
7	1	1	1	1	1	0	0	1	0	-2
@	8	1	1	1	0	1	0	1	0	-1
:	9	1	0	1	0	1	0		0	+4
>	Ø	1	0	1	0	0	0	1	0	0
.	-	0	1	1	1	1	1		2	0
<	#	1	1	1	0	0	0		0	+5
/	&	1	1	0	1	1	1		1	0
<	&	0	0	0	1	1	1		3	0
Δ	$	0	0	1	0	0	0		2	+5
#	,	1	0	0	0	0	0		1	+5
#	.	0	1	0	0	0	0		3	+5
■	/	1	0	0	1	1	1	1	1	-5

CORRESPONDENCE KEYBOARD

UC	LC	T2	CK	T1	R2A	R1	R2	R5	TLT	ROT
A	a	0	0	1	1	0	0	1	2	-2
B	b	1	0	0	0	0	0		1	+5
C	c	1	0	0	1	0	0	1	1	-2
D	d	1	1	0	1	1	0	1	1	-3
E	e	1	0	0	1	1	0		1	+2
F	f	0	0	0	1	0	1	1	3	-4
G	g	0	1	0	1	1	1	1	3	-5
H	h	1	1	0	0	1	0		1	+4
I	i	0	1	1	1	0	0		2	+3
J	j	0	0	0	1	1	1		3	0
K	k	1	1	0	1	0	0		1	+3
L	l	1	0	0	0	1	0	1	1	-1
M	m	0	0	1	1	1	1	1	2	-5
N	n	1	0	0	1	0	1		1	+1
O	o	0	0	1	0	1	0	1	2	-1
P	p	0	1	0	1	1	0		3	+2
Q	q	0	0	0	1	0	0		3	+3
R	r	0	1	1	1	1	0	1	2	-3
S	s	0	1	1	0	1	0		2	+4
T	t	1	1	0	1	1	1		1	0
U	u	1	1	0	1	0	1	1	1	-4
V	v	0	1	1	1	0	1	1	2	-4
W	w	0	0	1	0	0	0		2	+5
X	x	1	0	0	1	1	1	1	1	-5
Y	y	0	0	0	0	1	0		3	+4
Z	z	1	0	1	1	1	1		0	0
[	]	1	1	1	1	1	1	1	0	-5
@	2	1	1	1	1	0	1		0	+1
#	3	1	0	1	1	0	1	1	0	-4
$	4	1	1	1	0	1	0	1	0	-1
%	5	1	1	1	1	1	0		0	+2
ç	6	1	0	1	1	0	0		0	+3
&	7	1	0	1	1	1	0	1	0	-3
*	8	1	1	1	1	0	0	1	0	-2
(	9	1	1	1	0	0	0		0	+5
)	0	1	0	1	0	1	0		0	+4
_	-	0	1	0	0	0	0		3	+5
+	=	0	1	0	1	0	1		3	+1
°	!	0	1	1	1	1	1		2	0
:	;	0	0	0	1	1	0	1	3	-3
"	'	0	0	1	1	1	0		2	+2
,	,	0	1	0	1	0	0	1	3	-2
.	.	0	0	1	1	0	1		2	+1
?	/	0	1	0	0	1	0	1	3	-1

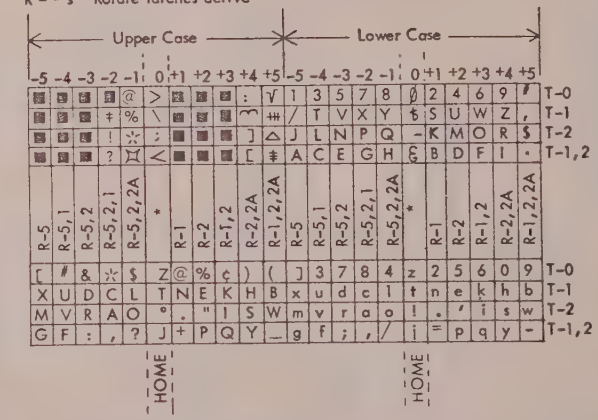
0 in Square* – Indicates; 1. Selection Contact Transferred
2. Latches Active

1 in Square* – Indicates Selection Magnet Energized

*Exception – 1 in R5 Column Indicates Both Selection Contact Transferred and Magnet Energized.

TYPEHEAD LAYOUT

T – #'s = Tilt latches active
R – #'s = Rotate latches active



*With one exception, home position (Zero Rotate) selection on the 73 Series is identical to the 72 Series – i.e., no rotate latches are active. On the BCD machine, home position, during zero tilt only (Ø & >), is selected by the cancellation method – i.e., all rotate latches are active (see the Selection and Tilt Rotate Schedule

On the BCD machine only, the symbol > (Greater Than) may be used to check type head homing.

0526-13684

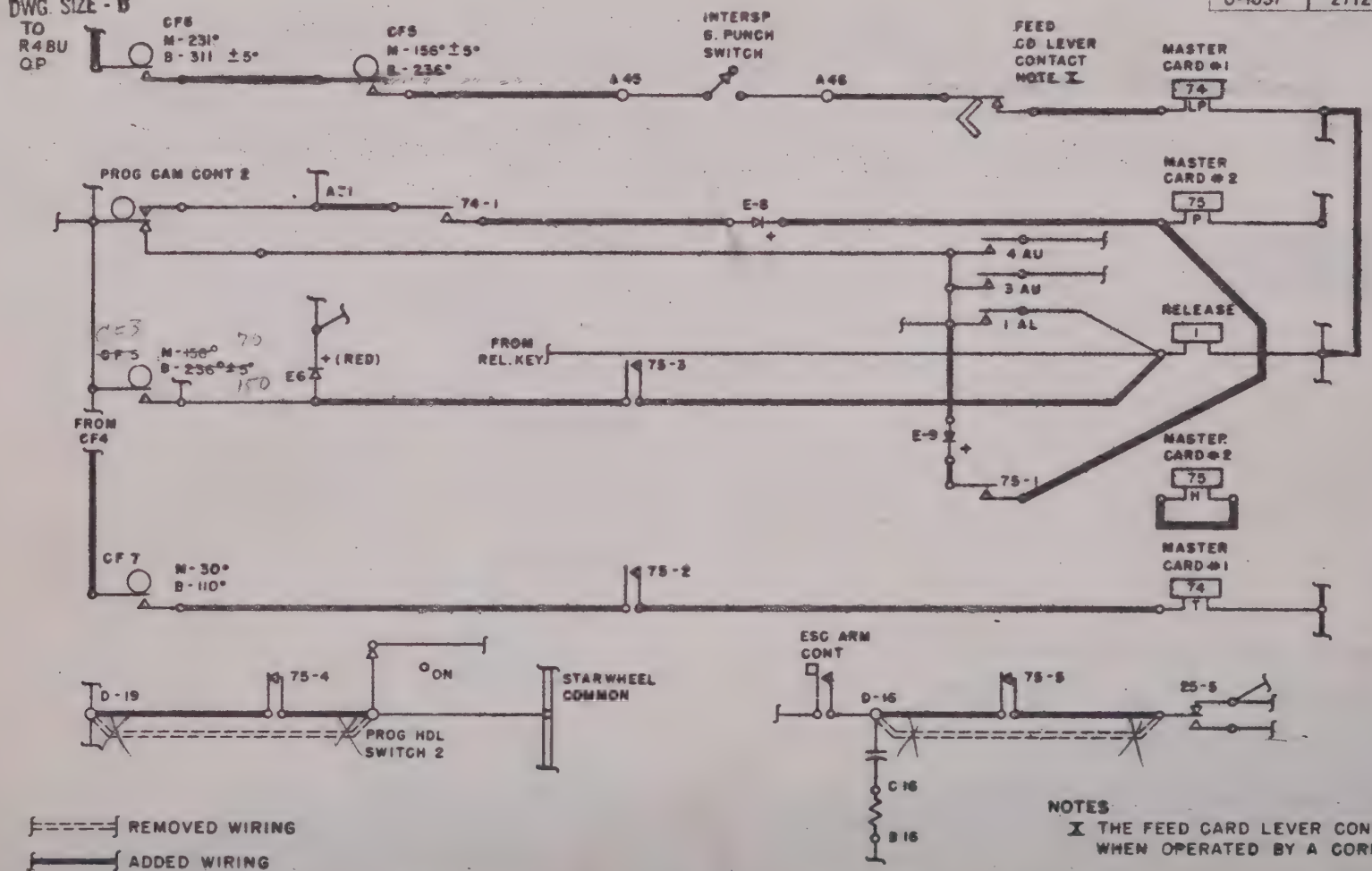
WAS 4121-333

STANDARD CODE	RELEASE NO.	REV. NO.	262915
0-1037	271288	1	

DATE	CHANGE NO.
3-27-53	81756
8-24-53	50715
10-8-53	50839
1-25-54	50787
9-28-55	290271
1-3-59	291551
6-18-60	202246A
6-16 61	292589
3-13-64	205006

MASTER

DWG. SIZE - B



RELAY NO.	PART NO.
74	186685
75	196198

REMOVED WIRING

ADDED WIRING

STANDARD WIRING

NOTES

X THE FEED CARD LEVER CONTACT MUST MAKE BY 228° +0 -2° WHEN OPERATED BY A CORNER CUT CARD

MATERIAL SPECIFICATION NO.	TOLERANCE UNLESS OTHERWISE NOTED	ALIGNMENT WITHIN NOTE I	INTERNATIONAL BUSINESS MACHINES CO.
CASE DEPTH	DECIMALS ± .005	CONCENTRIC WITHIN TOT IND READING NOTE II	MACH. PRINTING SUMMARY PUNCH
HARDNESS	FRACTIONS ± 1/64	FLAT WITHIN NOTE III	MODEL 526
SURFACE TREATMENT SPEC NO.	ANGLES ± 2°	PARALLEL WITHIN NOTE IV	NAME CIRCUIT DIAGRAM - INTERSPERSED GANG PUNCH
TECH RESEARCH APPR. DATE	CORNERS MAY BE BROKEN OUTSIDE INSIDE	STRAIGHT WITHIN NOTE V	DRAWN REM 10-14-52 SCALE NONE
I MH 3-30-53		SQUARE WITHIN In INCHES NOTE VI	CHK'D RDC 3-30-53 APPR. EJS 4-13-53

262915

262915

XBB-3520

CFS [illegible] 23+-0

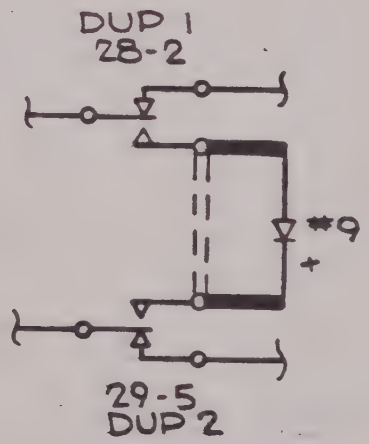
CFS [illegible] 231-0

Carl [illegible] 226 +0

0526
13684

STANDARDS CODE		TECHNICAL SERVICES APPRO			SYM	DATE	CHANGE NO	TECH APPRO	SYM	DATE	CHANGE NO	TECH APPRO	DEVELOPMENT NO	Q/M
		ELEC				72362	203867							
RELEASED FOR ASM	QTY	MET												
		PLASTIC											352979	
		FINISH												

MASTER



STANDARD WIRING

ADDED WIRING

REMOVED WIRING

GOOD FOR WIRING DIAGRAM
315911

IBM MATERIAL	NO
CASE DEPTH	
HARDNESS	
SURFACE TREATMENT	
6467SS	A

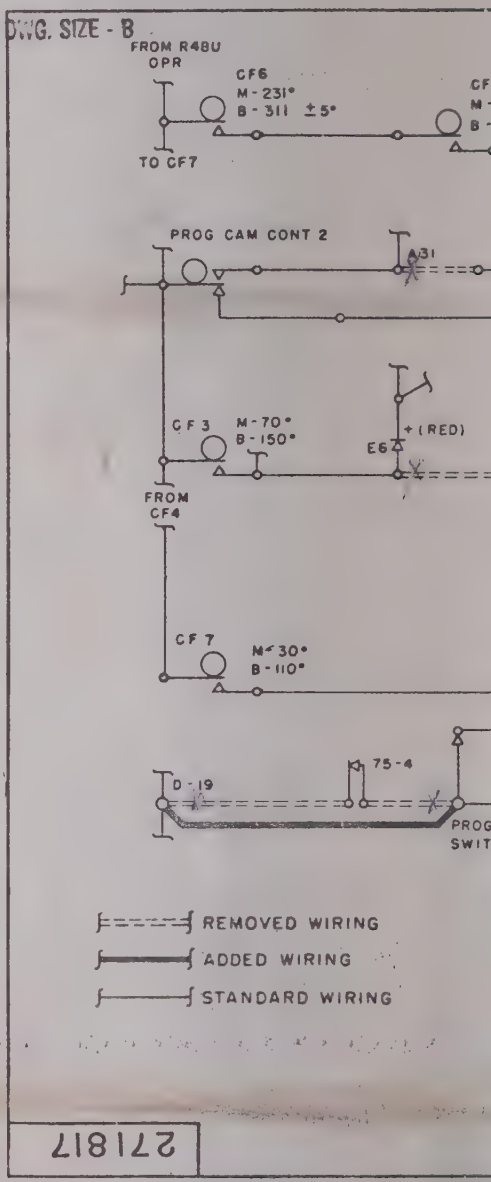
TOLERANCE UNLESS OTHERWISE NOTED	
2 PLACE DEC ±	
3 PLACE DEC ±	
ANGLES ±	
CORNERS AND/OR	OUTSIDE MAX

MUST CONFORM TO ENG SPEC	
ALIGNMENT WITHIN	NOTE I
CONC TO DU WITHIN	TIR NOTE II
FLAT WITHIN	NOTE III
PARALLEL TO DU WITHIN	NOTE IV
STRAIGHT WITHIN	

INTERNATIONAL BUSINESS MACHINES CORP

NAME	SUPPLEMENT W/D				
DESIGN	JUR	3-7-61	TYPE	526	
DETAIL	JUR	3-7-61	SCALE	NONE	

1525
1525


DWG. SIZE - B
FROM R48U
OPR
CF6
M - 231°
B - 311 ±5°
TO CF7
PROG CAM CONT 2
A31
CF 3
M - 70°
B - 150°
E6
+ (RED)
FROM
CF4
CF 7
M - 30°
B - 110°
D - 19
75-4
REMOVED WIRING
ADDED WIRING
STANDARD WIRING
271817

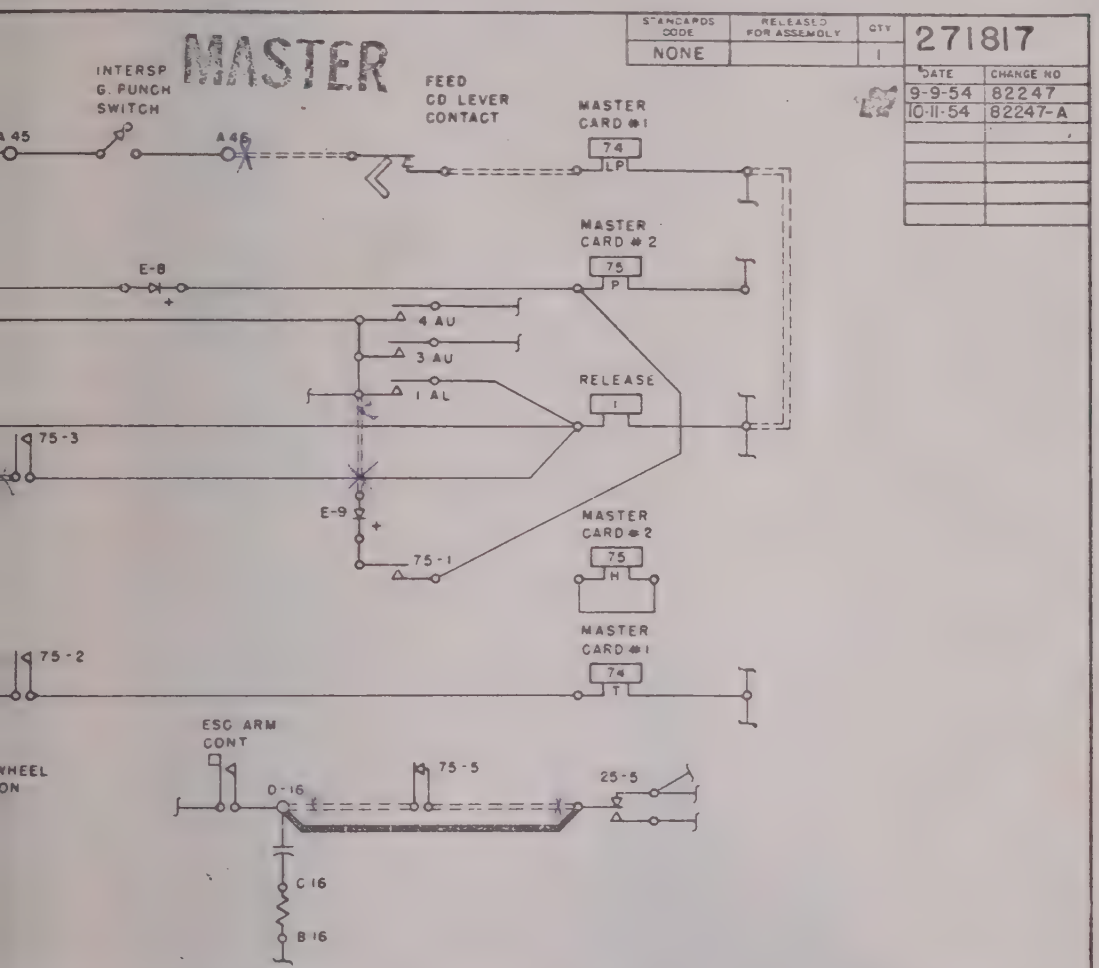
0526
13687

٥٢٤٥
١٣٥٤٨

STANDARDS CODE	RELEASED FOR ASSEMBLY	QTY
NONE		1

271817

DATE	CHANGE NO
9-9-54	82247
10-11-54	82247-A



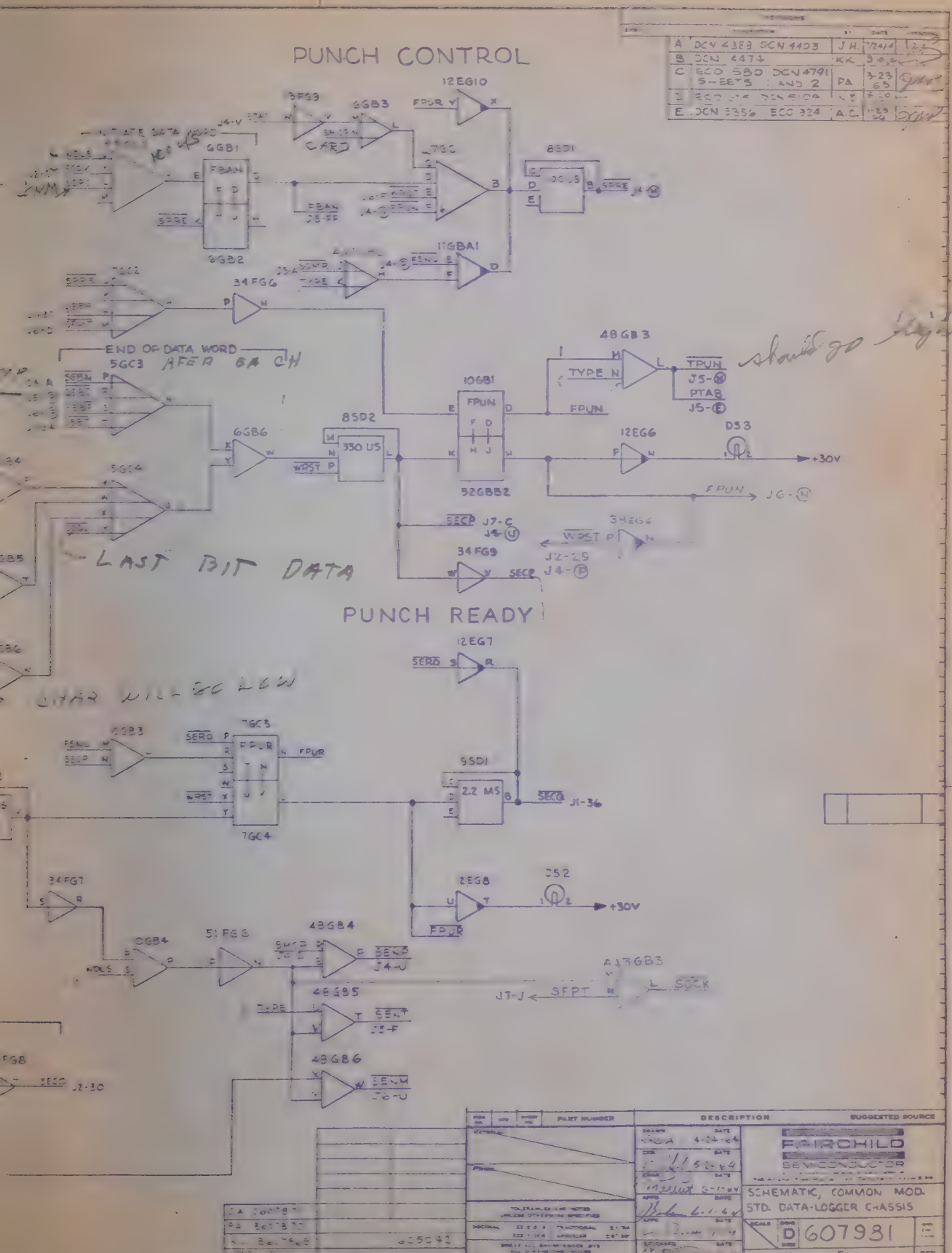
TOLERANCE UNLESS OTHERWISE NOTED			
DECIMALS ± .005	ALIGNMENT WITHIN		NOTE I
FRACTIONS ± 1/64	CONCENTRIC WITHIN	TOT IND READING	NOTE II
ANGLES ± 2	FLAT WITHIN		NOTE III
CORNERS MAY BE BROKEN: OUTSIDE / INSIDE	PARALLEL WITHIN		NOTE IV
	STRAIGHT WITHIN		NOTE V
	SQUARE WITHIN	INCHES	NOTE VI

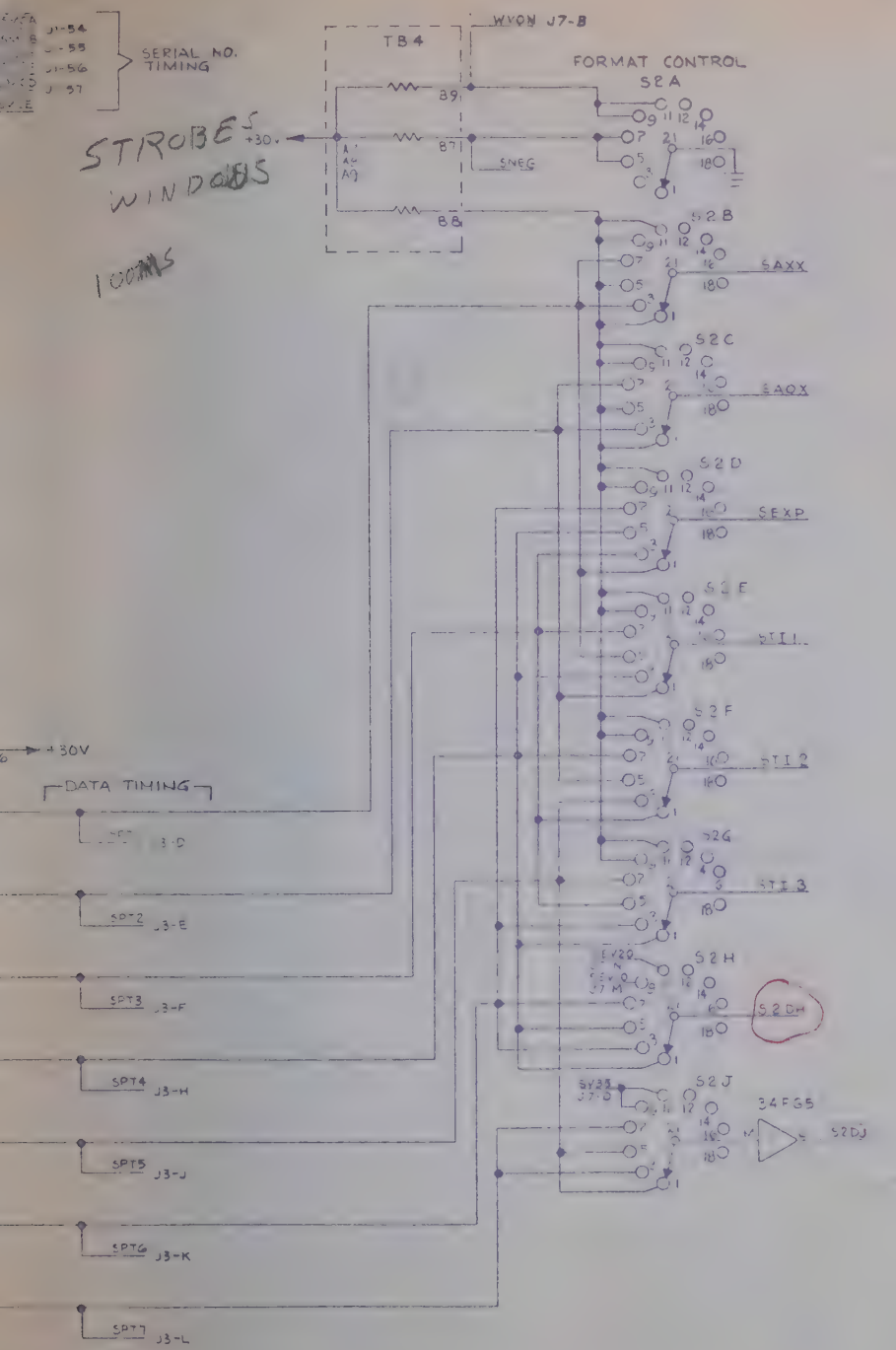
INTERNATIONAL BUSINESS MACHINES CORP.

MACH	PRINTING SUMMARY PUNCH				
MODEL	526				
NAME	CIRCUIT DIAGRAM - CR INTERSPERSED GANG PUNCH				
DRAW	ERS	9-2-54	SCALE	NONE	
CHECK	LFT	9-2-54	TRAC	MD	9-9-54
APPRO	RTB	9-2-54	CHECK		9 10 54

271817

SPEC NO
TECH RESEARCH
APPRO DATE


PUNCH CONTROL
12EG10
3FG9
6GB3
FPUR
CARD
6GB1
FBAN
6GB2
7GC
8SD1
34FG6
7GC2
END OF DATA WORD
5GC3
AFER 6A CH
48GB3
TYPE N
TPUN
J5-M
PTAB
J5-E
10GB1
FPUN
8SD2
330 US
6GB6
5GC4
12EG6
DS3
+30V
52GBB2
SECP J7-C
J4-U
34EG6
WRST P
J2-29
34FG9
J4-B
LAST BIT DATA
PUNCH READY
12EG7
SERO
7GC3
6GB3
7GC4
9SD1
2.2 MS
SECO J1-36
34FG7
2EG8
DS2
+30V
6GB4
51FG8
48GB4
SBEN
J4-U
48GB5
SENT
J5-F
48GB6
SENM
J6-U
SFPT
L SOCK
DESCRIPTION
FAIRCHILD
SEMICONDUCTOR
SCHEMATIC, COMMON MOD.
STD. DATA-LOGGER CHASSIS
607931
SHEET 1 OF 5



REVISIONS

BY	DESCRIPTION	BY	DATE	APPROVAL

PART NUMBER	DESCRIPTION	SUGGESTED SOURCE

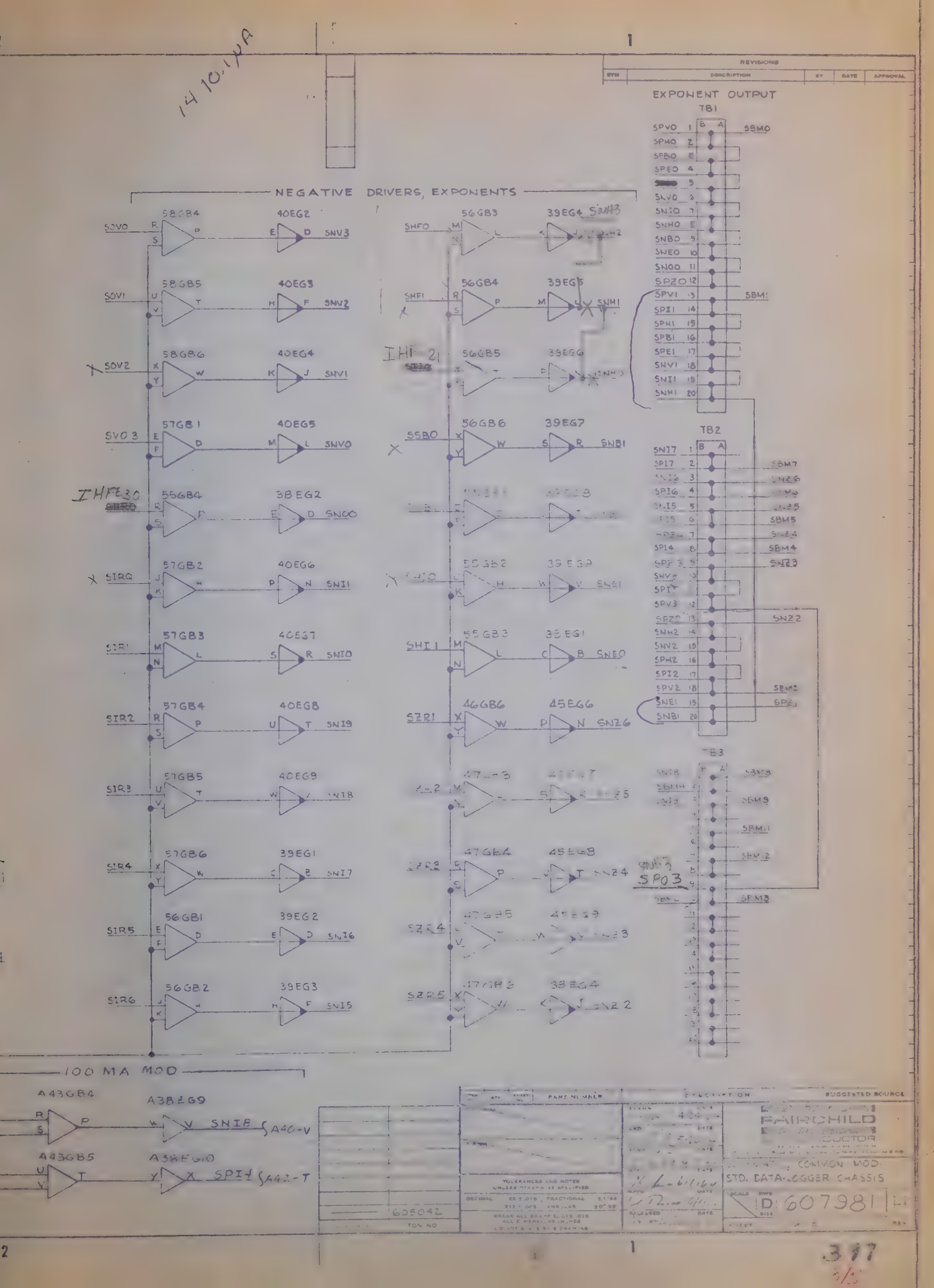
FAIRCHILD

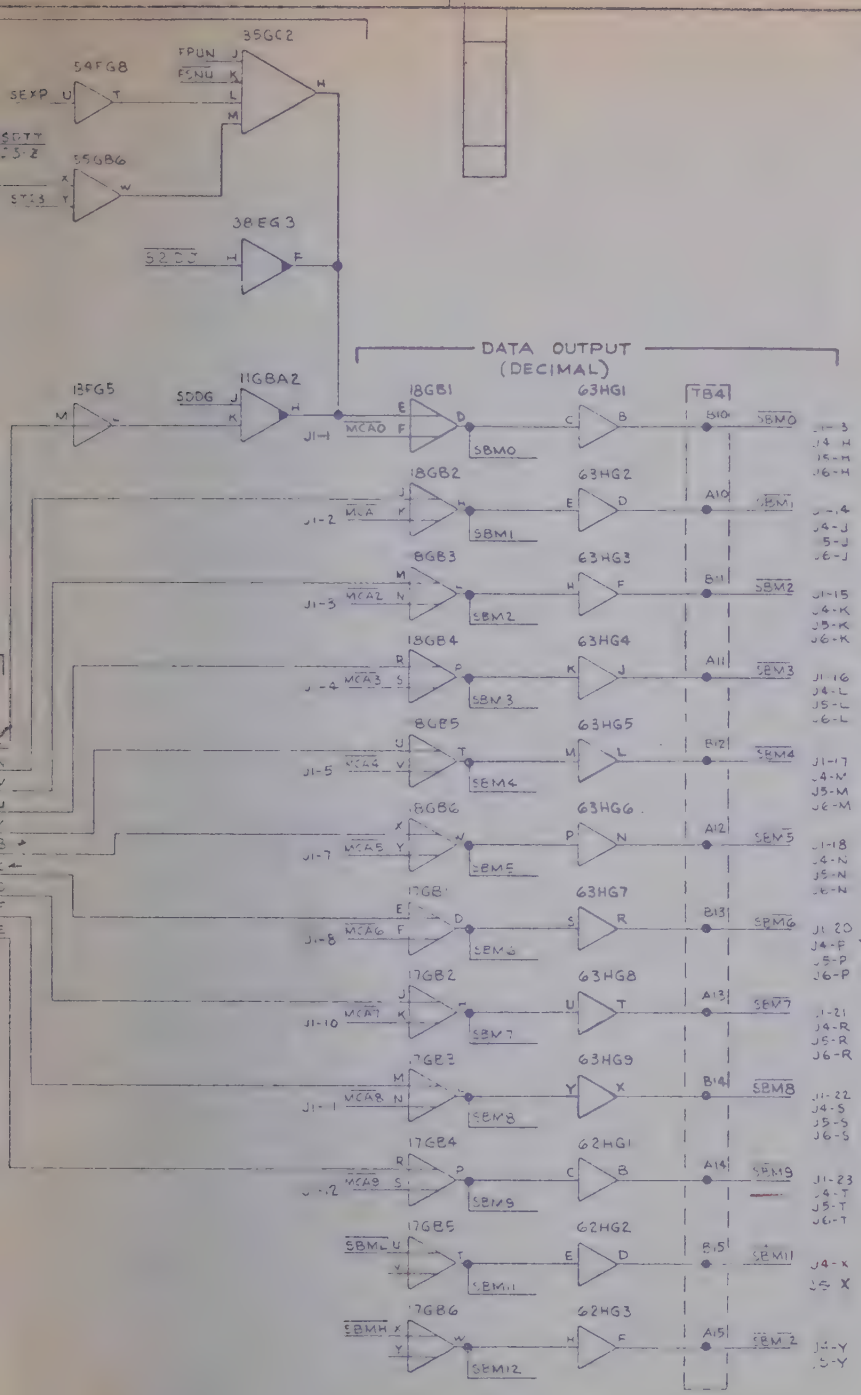
SCHEMATIC, COMMON MOD. STD. DATA-LOGGER CHASSIS

SIZE D

SHEET OF

396


EXPONENT OUTPUT
TB1
TB2
TB3
NEGATIVE DRIVERS, EXPONENTS
100 MA MOD
58GB4
40EG2
SOV0
SNV3
58GB5
40EG3
SOV1
SNV2
58GB6
40EG4
SOV2
SNV1
57GB1
40EG5
SOV3
SNV0
56GB4
38EG2
SNO0
57GB2
40EG6
SIRQ
SNI1
57GB3
40EG7
SIR1
SNI0
57GB4
40EG8
SIR2
SNI9
57GB5
40EG9
SIR3
SNI8
57GB6
39EG1
SIR4
SNI7
56GB1
39EG2
SIR5
SNI6
56GB2
39EG3
SIR6
SNI5
56GB3
39EG4
SNH3
SMF0
56GB4
39EG5
SMF1
SNH1
56GB5
39EG6
IHI 21
56GB6
39EG7
SSB0
SNB1
55GB3
38EG1
SHI1
SNE0
46GB6
45EG6
SZR1
SNZ6
A43GB4
A38EG9
SNI8
A40-V
A43GB5
A38EG10
SPI1
A44-T
605042
FAIRCHILD
COMMON MOD.
STD. DATA-LOGGER CHASSIS
D 607981



ITEM	QTY	PART NUMBER	DESCRIPTION	SUGGESTED SOURCE

FAIRCHILD SEMICONDUCTOR

SCHEMATIC, COMMON MOD STD DATA-LOGGER CHASSIS

SCALE DWG D 607981 H

SHEET OF REV

398

REVISIONS

SYM	DESCRIPTION	BY	DATE	APPROVAL

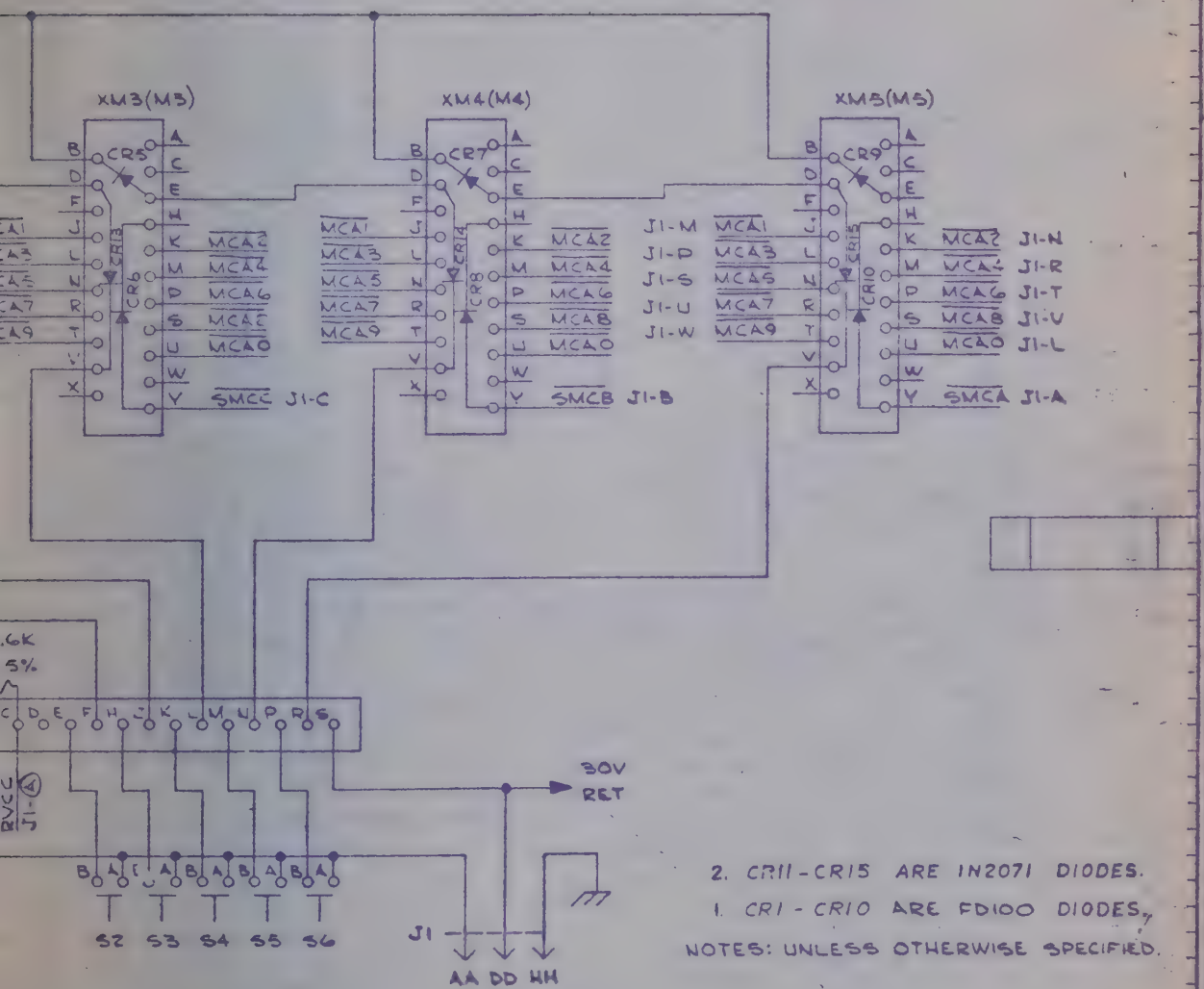
DESCRIPTION

FAIRCHILD
SEMICONDUCTOR

SCHEMATIC, COMMON MOD.
STD. DATA-LOGGER CHASSIS

399

REVISIONS				
SYM	DESCRIPTION	BY	DATE	APPROVAL
A	DCN 4550, ECO 448	J.H.	1/12/64	
B	DCN 4695 ECO 510	K Kuo	1-27-66	



2. CR11-CR15 ARE IN2071 DIODES.

1. CR1-CR10 ARE FD100 DIODES.

NOTES: UNLESS OTHERWISE SPECIFIED.

FOR SERIAL No. ONLY

ITEM NO	QTY.	SHEET NO	PART NUMBER	DESCRIPTION	SUGGESTED SOURCE

MATERIAL:

FINISH

TOLERANCES AND NOTES UNLESS OTHERWISE SPECIFIED
DECIMAL .XX ±.010 FRACTIONAL ±1/64
XXX ±.005 ANGULAR ±0° 30'
BREAK ALL SHARP EDGES .010
ALL DIMENSIONS INCHES
DO NOT SCALE THIS DRAWING

DRAWN W. ISERI 9.9.64
CHK. 9.23.64
ENGR. 9-23-64
APPD. DATE
APPD. DATE
RELEASED 9.24.64

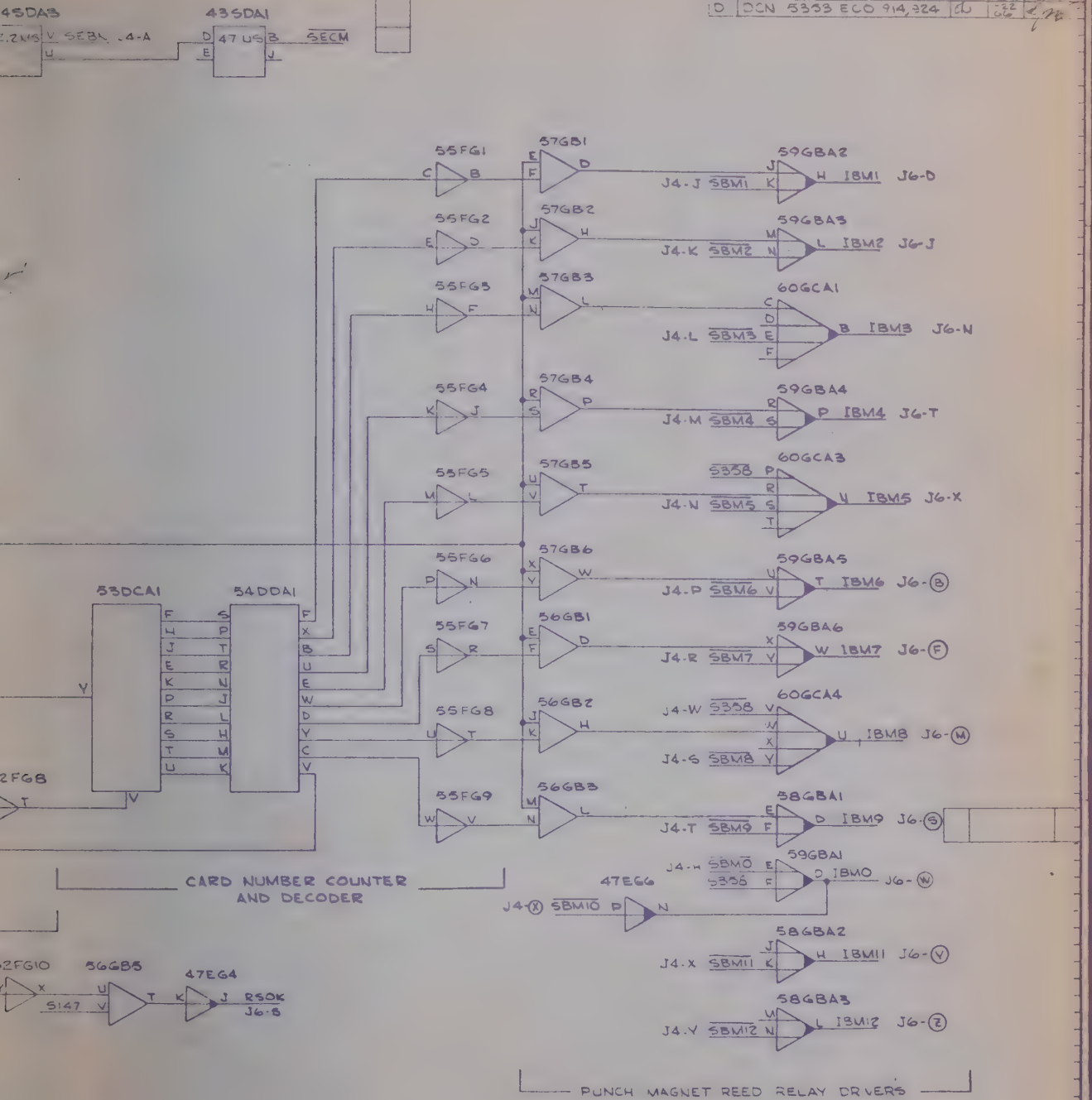
FAIRCHILD SEMICONDUCTOR

SCH., DEVICE COUNTER DATA LOGGER CHASSIS

SCALE — DWG. C SIZE 608455 B
SHEET 1 OF 1 REV

08460		605046
08456	D608333	605110
DWG	NEXT ASSY	TDN NO

REVISIONS				
SYM	DESCRIPTION	BY	DATE	APPROVAL
A	DCN 4653 ADDED REF DESIGS.	CW	11-3-65	
B	DCN 4790 ECO 572	KK	12-5-65	
C	DCN 4826 ECO 663	RW	9-3-66	
D	DCN 5333 ECO 914, 924	CW	12-22-66	



ITEM NO.	QTY	SHEET	PART NUMBER	DESCRIPTION	SUGGESTED SOURCE
			MATERIAL		
			FINISH		

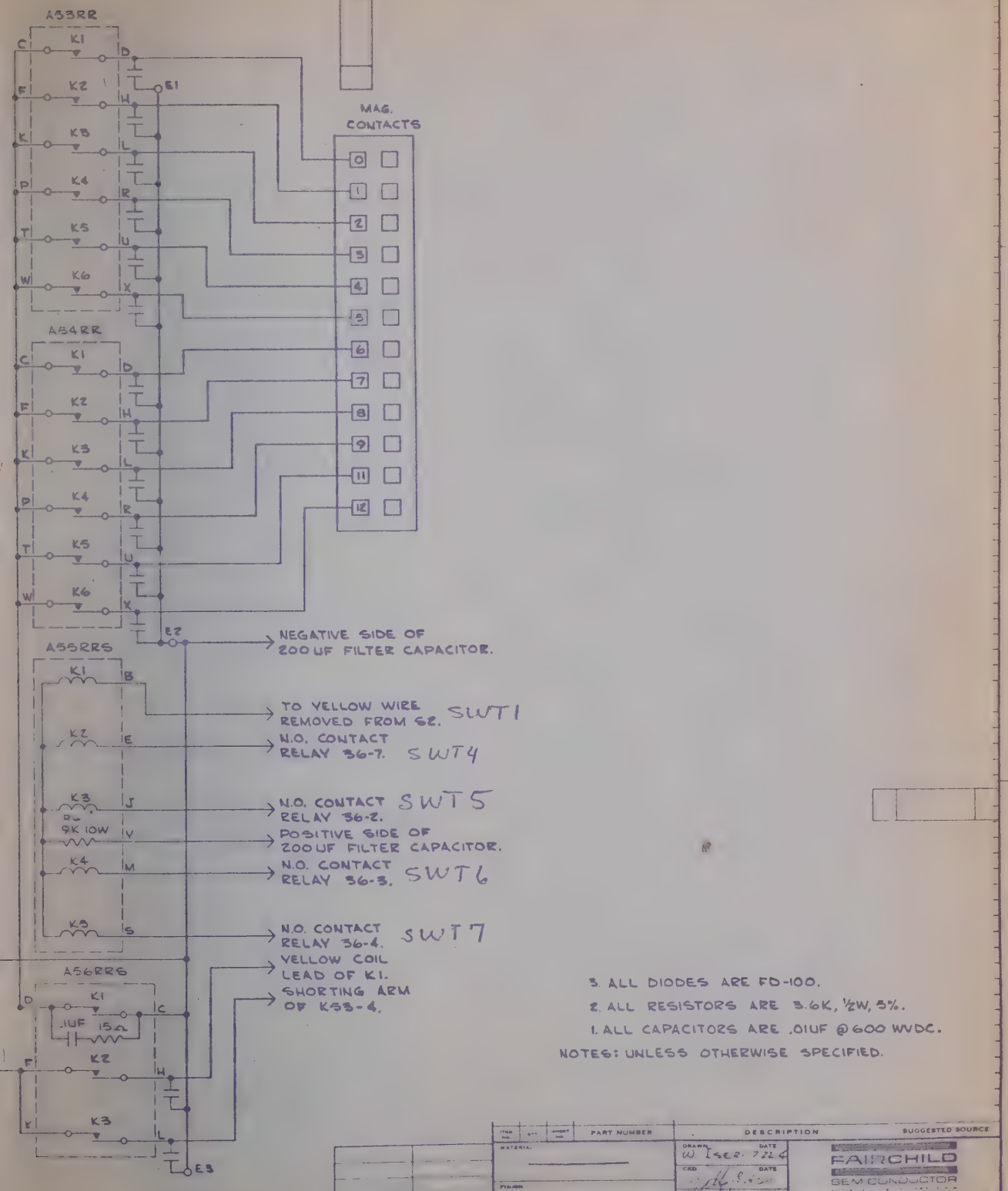
TOLERANCES AND NOTES UNLESS OTHERWISE SPECIFIED
DECIMAL .XX ±.010 FRACTIONAL ±1/64
.XXX ±.005 ANGULAR ±0° 30'
BREAK ALL SHARP EDGES .010
ALL DIMENSIONS IN INCHES
DO NOT SCALE THIS DRAWING

DRAWN J. ISERI 7-24-64
CHKD. DATE
ENGR. DATE
APPD. DATE
APPD DATE
RELEASED DATE

FAIRCHILD SEMICONDUCTOR
SCH, 526 DTL CARD PUNCH BUFFER MODULE
SCALE DWG D 603339 REV
SHEET 1 OF 1

C.A. C608442		
W.L. B608460	D608457	605110
REF. DWGS.	NEXT ASSY	TDN NO

405



3. ALL DIODES ARE FD-100.
2. ALL RESISTORS ARE 3.6K, ½W, 5%.
1. ALL CAPACITORS ARE .01UF @ 600 WVDC.

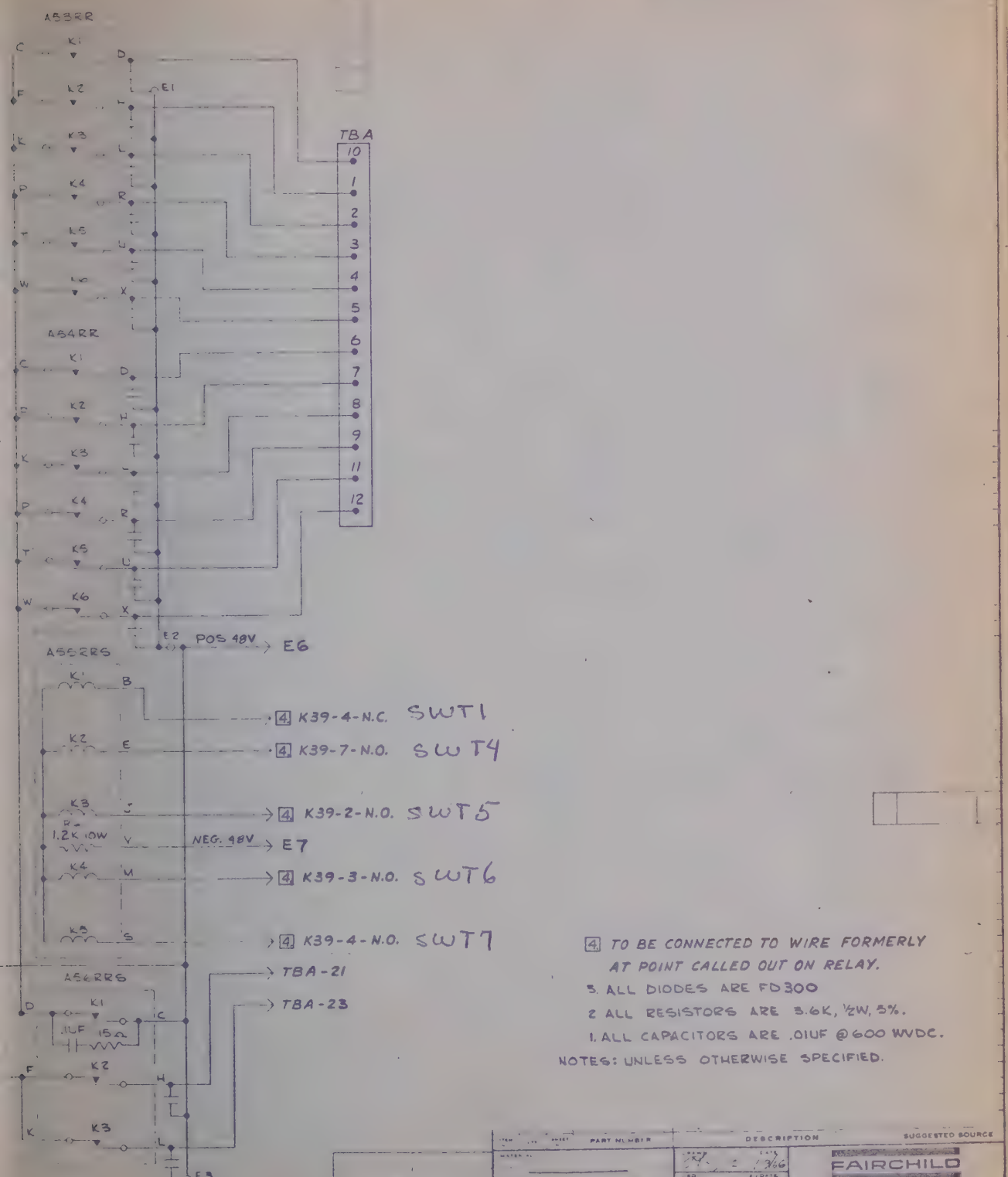
NOTES: UNLESS OTHERWISE SPECIFIED.

DESCRIPTION	SUGGESTED SOURCE
DRAWN W Isee 7-22-64	FAIRCHILD SEMICONDUCTOR
CKD	SCHEMATIC
APPD. 7-27-64	026 CARD PUNCH RELAYS
APPD.	SCALE — DWG D 608339 D
RELEASED	SHEET 2 OF 3 REV

MATERIAL — FINISH — TOLERANCES AND NOTES UNLESS OTHERWISE SPECIFIED — DECIMAL XX ± .010 FRACTIONAL ± 1/64 XXX ± .005 ANGULAR 30' BREAK ALL SHARP EDGES .010 ALL DIMENSIONS INCHES DO NOT SCALE THIS DRAWING

NEXT ASSY 605110 TOP NO.

406

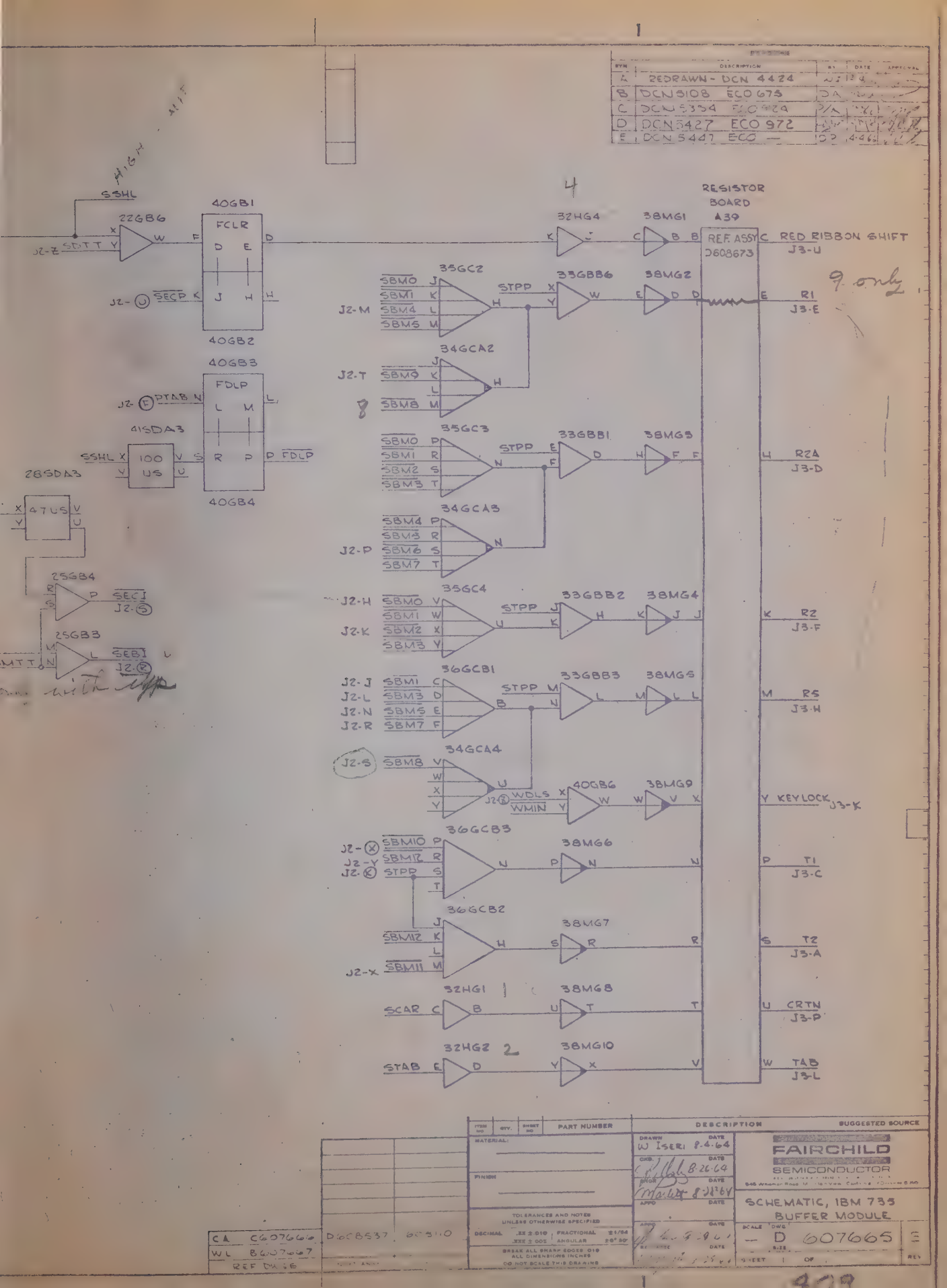


4. TO BE CONNECTED TO WIRE FORMERLY AT POINT CALLED OUT ON RELAY.
3. ALL DIODES ARE FD300
2. ALL RESISTORS ARE 3.6K, ½W, 5%.
1. ALL CAPACITORS ARE .01UF @ 600 WVDC.

NOTES: UNLESS OTHERWISE SPECIFIED.

W.L.B. 810250
REF. DWG.
810249

FAIRCHILD
SCHEMATIC
526 CARD PUNCH RELAYS
D 608339 D
SHEET 3 OF 3


SYM | DESCRIPTION | BY | DATE | APPROVAL
A | REDRAWN - DCN 4424 | WJ | 12-4
B | DCN 5108 ECO 675 | DA | 3-26-64
C | DCN 5334 ECO 829 | DA | 7-6-64
D | DCN 5427 ECO 972 | DA | 11-4-64
E | DCN 5447 ECO — | DA | 4-4-66
RESISTOR BOARD A39
REF ASSY D608673
SSHL
40GB1
22GB6
FCLR
40GB2
40GB3
FDLP
41SDA3
100 US
40GB4
28SDA3
47 US
25GB4
25GB3
32HG4
38MG1
32HG2
38MG2
32HG1
38MG8
32HG2
38MG10
35GC2
33GBB6
35GC3
33GBB1
34GCA2
34GCA3
35GC4
33GBB2
36GCB1
33GBB3
34GCA4
40GB6
38MG9
36GCB3
38MG6
36GCB2
38MG7
J2-Z SDTT
J2-U SECP
J2-T STAB
J2-S SECI
J2-R SEBI
J2-M SBM0 SBM1 SBM4 SBM5
J2-T SBM9 SBM8
J2-P SBM4 SBM5 SBM6 SBM7
J2-H SBM0 SBM1 SBM2 SBM3
J2-K SBM2 SBM3
J2-J SBM1
J2-L SBM3
J2-N SBM5
J2-R SBM7
J2-S SBM8
J2-R WDLS WMIN
J2-X SBM10 SBM12 SBM11
J2-Y SBM12
J2-K STPP
SCAR
STAB
C RED RIBBON SHIFT J3-U
E RI J3-E
H RZA J3-D
K RZ J3-F
M RS J3-H
Y KEYLOCK J3-K
P TI J3-C
S TZ J3-A
U CRTN J3-P
W TAB J3-L
9 only
8
2
CA C607666
WL B607667
REF DWG
D608537 607510
PART NUMBER
DESCRIPTION
SUGGESTED SOURCE
MATERIAL:
FINISH
TOLERANCES AND NOTES UNLESS OTHERWISE SPECIFIED
DECIMAL .XX ± .010 | FRACTIONAL 1/64
.XXX ± .005 | ANGULAR 2° 30'
BREAK ALL SHARP EDGES .010
ALL DIMENSIONS INCHES
DO NOT SCALE THIS DRAWING
DRAWN W Iseri 8.4.64
CKD 8.26.64
ENGR 8.27.64
APPD
FAIRCHILD SEMICONDUCTOR
SCHEMATIC, IBM 735 BUFFER MODULE
SCALE — DWG D 607665
SHEET 1 OF 1
REV E

			m			DIX			m				
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			p						p				
			r						r				
			s						s				
			t						t				
			u						u				
			v						v				
			w						w				
			x						x				
			y						y				
			z						z				
			AA						AA				
			BB						BB				
			CC						CC				
			DD						DD				
			EE						EE				
			FF						FF				
			HH						HH				

						DRAWN DATE
						CKD
						ENGR
						APPD
TDN						APPD
ASSY	REV	DESCRIPTION	BY	DATE	APP	RELEASED

FAIRCHILD
INSTRUMENTATION
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CABLE LIST DATA

500

SIZE B

SHEET 1 OF 2

REV

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			z		
			AA		
			BB		
			CC		
			DD		
			EE		
			FF		
			HH		

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						ENGR
						APPD
TDN						APPD
ASSY.	REV	DESCRIPTION	BY	DATE	APP	RELEASED

FAIRCHILD
INSTRUMENTATION

CABLE LIST DATA

SIZE B

SHEET 2 OF 5

REV

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			FF		
			HH		

						DRAWN	DATE
						CKD	
						ENGR	
						APPD	
TDN						APPD	
ASSY	REV	DESCRIPTION	BY	DATE	APP	RELEASED	

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CABLE LIST

SIZE B | 30801-3

SHEET 5 OF 5 | REV

			35		
			36		
			37		
			38		
			39		
$\overline{SZR1}$	OPT	24	40	33-P6	H
$\overline{SZR2}$	↑	↑	41	↑	J
$\overline{SZR3}$	↕	↕	42	↕	M
$\overline{SZR4}$	↓	↓	43	↓	N
$\overline{SZR5}$	OPT	24	44	33-P6	P
			45		
			46		
			47		
			48		
			49		
			50		

						DRAWN	DATE
						E Robertson	10-19-66
						CKD	
						ENGR	
						APPD	
TDN						APPD	
ASSY	REV	DESCRIPTION	BY	DATE	APP	RELEASED	

FAIRCHILD
SEMICONDUCTOR
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CABLE LIST DATA-LOG
500 SN# 036

SIZE B | 608011-3 | REV

SHEET 3 OF 5

SA01			36		X
SA02			37		Y
SA04			38	↓	Z
SA03			39	31 - P5	AA
STIQ			40	33 - P6	L
SHFO			41	33 - P6	A
SHFI			42	33 - P6	B
FHIF			43	31 - P5	HH
FLOF			44	31 - P5	C
SHRO			45	33 - P6	K
SIRQ			46	31 - P5	D
SSBO			47	33 - P6	C
SSBI			48	↑	D
SHIO	↓	↓	49	↓	E
SHI1	OPT	24	50	33 - P6	F

						DRAWN	DATE
						E. Kobayashi	10-13-66
						CKD	
						ENGR	
						APPD	
TDN						APPD	
ASSY	REV	DESCRIPTION	BY	DATE	APP	RELEASED	

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500 SN# 036

SIZE B | 608011-3 | REV

SHEET 4 OF 5

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